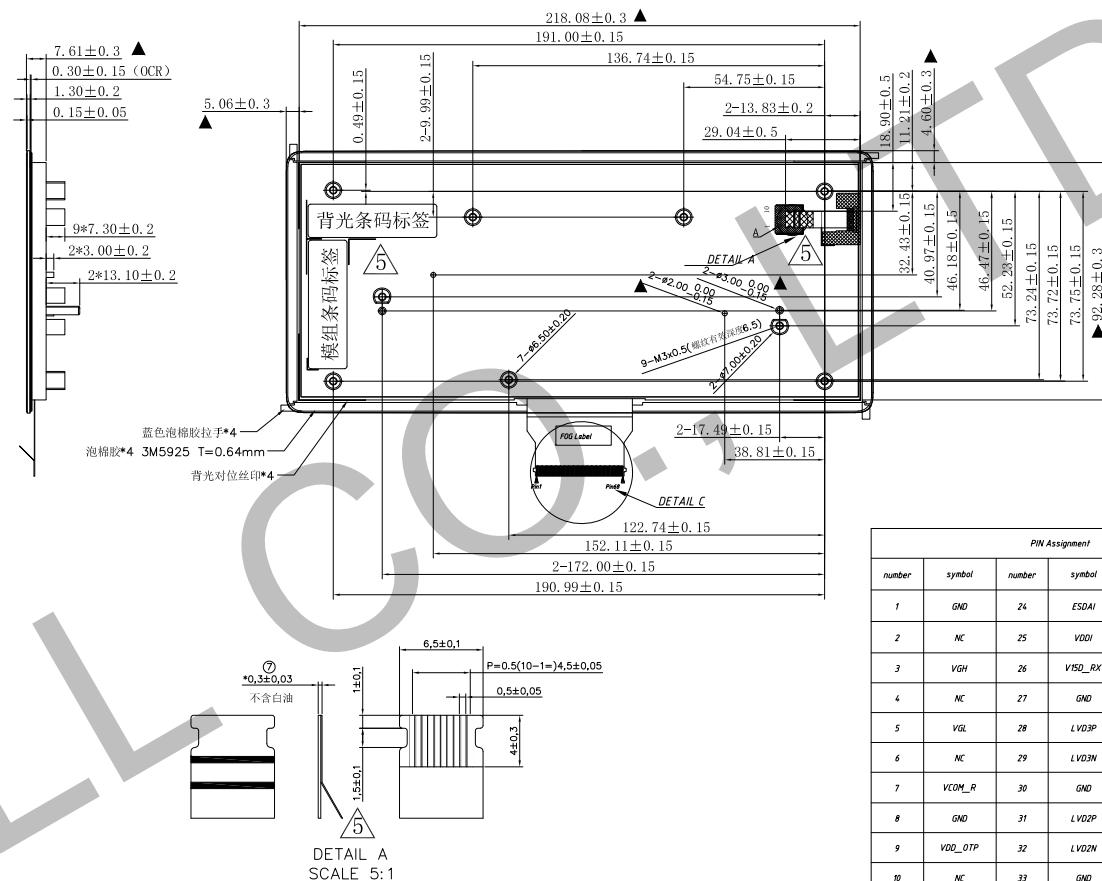


101097-206850-HF



PIN Assignment					
number	symbol	number	symbol	number	symbol
1	GND	24	ESDAI	47	VGMHIO
2	NC	25	VDDI	48	VGMNLO
3	VGH	26	VISO_RX	49	NC
4	NC	27	GND	50	VSN
5	VGL	28	LVDDP	51	NC
6	NC	29	LVDDN	52	VSP
7	VCOM_R	30	GND	53	NC
8	GND	31	LVDDP	54	FAR_DET
9	VDD_OTP	32	LVDDN	55	STBYB
10	NC	33	GND	56	RESETB
11	VSP	34	LVDDCKP	57	UD_LR
12	VRSPP	35	LVDDCKN	58	ROM_RLD
13	NC	36	GND	59	BIST
14	VRSNN	37	LVDDP	60	GND
15	VSN	38	LVDDN	61	VCOM_L
16	VCL	39	GND	62	NC
17	NC	40	LVDDP	63	NC
18	I2C_SDA	41	LVDDN	64	VGH
19	I2C_SCL	42	GND	65	NC
20	EEPEN	43	VDDI	66	VGL
21	ECS	44	VISO	67	NC
22	ESCL	45	VGMPHIO	68	GND
23	ESDAO	46	VGMPLIO		

BL PIN		14	VPSN	37	LVDIP	60	GND
1	A1	15	VSN	38	LVDIN	61	VCOM_L
2	A2	16	VCL	39	GND	62	NC
3	NC	17	NC	40	LVDOP	63	NC
4	NTC+	18	I2C_SDA	41	LVDON	64	VGH
5	NTC-	19	I2C_SCL	42	GND	65	NC
6	NC	20	EEPEN	43	VDDI	66	VGL
7	K1	21	ECS	44	V1SD	67	NC
8	K2	22	ESCL	45	VGMPH0	68	GND
9	K3	23	ESDA0	46	VGMPL0		
10	K4						

General specification

Item	specifications	Unit
Active screen size	8.8 inch Diagonal	-
Display resolution	1280x RGB x480	pixel
Module size (W x H x T)	228.20 ×101.71 × 7.61	mm
Active area (W x H)	209.28 x 78.48	mm
Pixel pitch (W×H)	0.1635x 0.1635	mm
Interface	LVDS	-
Module weight	TBD±10%	g
Our components and processes are compliant to RoHS standard.		

Item	specifications
LCD type	a-Si TFT
LCD mode	Normally Black
Polarizer mode	Transmissive
Pixel arrangement	RGB-stripe
Backlight type	LED
Viewing direction(Gray inversion)	88/88/88/88

Interface pin

Pin No.	Symbol	Function
1	GND	Ground
2	NC	No Connection
3	VGH	External power supply for gate driver
4	NC	Red Data
5	VGL	External power supply for gate driver
6	NC	Blue Data
7	VCOMR	Vcom power
8	GND	Ground
9	VDD_OTP	OTP PIN
10	NC	No Connection
11	VSP	External power supply for gate driver
12	VRSP	Regulator output for source driver
13	NC	No Connection
14	VRSN	Regulator output for source driver
15	VSN	Regulator output for source driver
16	VCL	Negative level shifter power
17	NC	Not connect
18	12C_SDA	Serial interface address and data
19	DC_SCL	Serial interface clock input
20	EEPEN	EEPROM reload control H: Enable EEPROM reload L: Disable EEPROM reload (Default)
21	ECS	EEPROM Control
22	ESCL	EEPROM Control
23	ESDAO	EEPROM Control
24	ESDAI	EEPROM Control
25	VDDI	Power supply 3.3V
26	V15D_RX	"Regulator output for digital power
27	GND	Ground
28	LVD3P	LVDS DATA3(+)
29	LVD3N	LVDS DATA3(-)
30	GND	Ground
31	LVD2P	LVDS DATA2(+)
32	LVD2N	LVDS DATA2(-)
33	GND	Ground
34	LVDCLKP	LVDS CLOCK(+)
35	LVDCLKN	LVDS CLOCK(-)
36	GND	Ground
37	LVD1P	LVDS DATA0(+)
38	LVD1N	LVDS DATA0(-)
39	GND	Ground
40	LVDOP	LVDS DATA0(+)
41	LVDON	LVDS DATA0(-)
42	GND	Not connect
43	VDDI	Ground
44	V15D	Regulator output for digital power supply
45	VGMPHO	Internal regulator output for positive gamma reference voltage
46	VGMPLO	Internal regulator output for positive gamma reference voltage
47	VGMNHO	Internal regulator output for positive gamma reference voltage
48	VGMNLO	Internal regulator output for positive gamma reference voltage
49	NC	No Connection
50	VSN	Power supply for analog power
51	NC	No Connection
52	VSP	Power supply for analog power
53	NC	No Connection
54	FAILDET	TFT fail detection output.
55	STBYB	STBYB Pin, Normal=High, STBYB=Low
56	RESETB	STBYB Pin, Normal=High, STBYB=Low
57	UD-LR	UD and LR Conned together, control the display scan direction
58	ROM_RLD	OTP or EEPROM reload per 30 fames
59	BIST	Built-In Self lest function Normal: L BIST:H
60	GND	Ground
61	VCOML	VCOM power
62~63	NC	No Connection
64	VGH	External power supply for gate driver
65	NC	No Connection
66	VGL	External power supply for gate driver
67	NC	No Connection
68	GND	GROUND