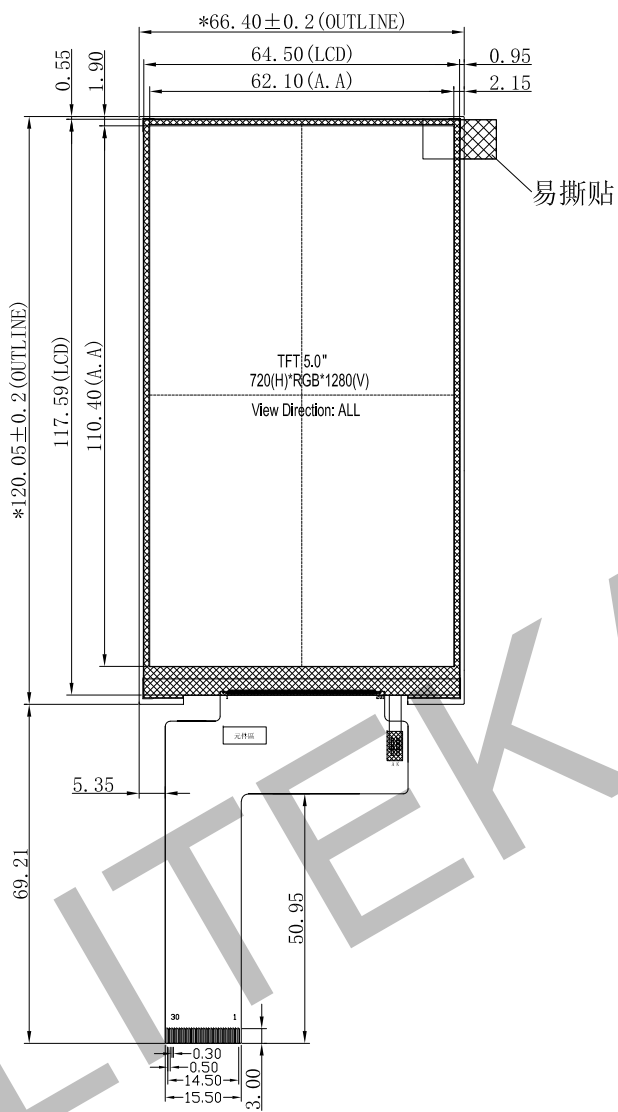
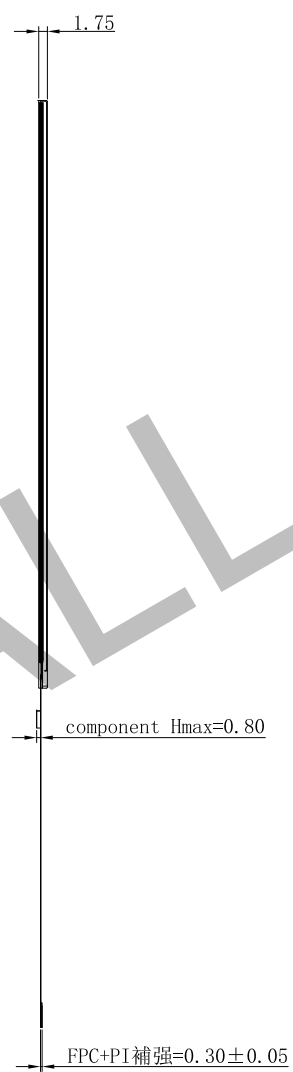


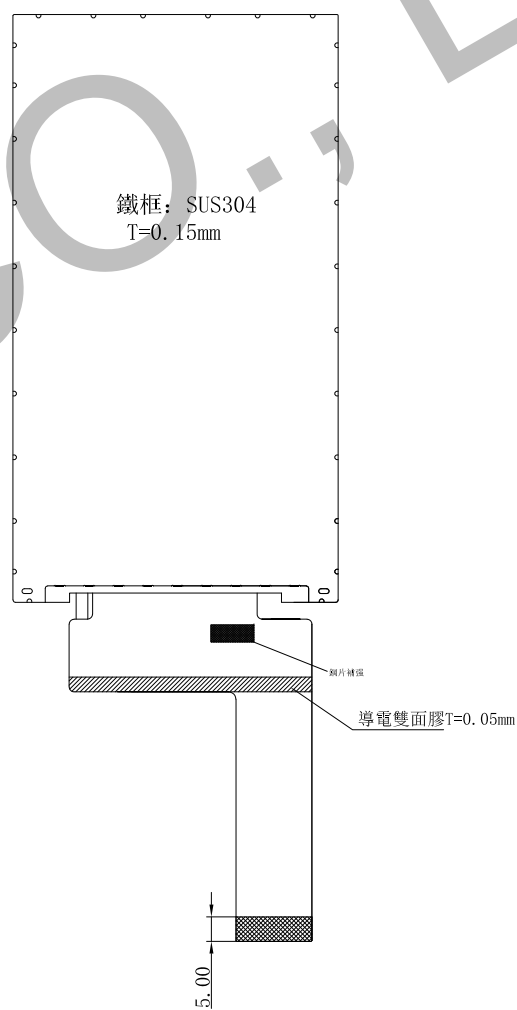
TOP VIEW



SIDE VIEW



BOTTOM VIEW



PIN FUNCTION

PIN:	SYMBLE
1	VDD
2	VDD
3	GND
4	RESET
5	NC
6	NC
7	GND
8	D2N
9	D2P
10	GND
11	D1N
12	D1P
13	GND
14	CLKN
15	CLKP
16	GND
17	D0N
18	D0P
19	GND
20	D3N
21	D3P
22	GND
23	IOVCC
24	K
25	K
26	K
27	K
28	A
29	A
30	A

General specification

Item	specifications	Unit
Active screen size	5.0 inch Diagonal	-
Display resolution	720 x RGB x1280	pixel
Module size (W x H x T)	66.4×120.05 × 2.05(Exclude FPC)	mm
Active area (W x H)	62.1 × 110.40	mm
Pixel pitch (W×H)	0.086 x 0.086	mm
Interface	MIPI 4	-
Module weight	TBD±10%	g
Our components and processes are compliant to RoHS standard.		

Item	specifications
LCD type	a-Si TFT
LCD mode	IPS / Normal Black
Polarizer mode	Transmissive
Pixel arrangement	RGB-stripe
Backlight type	LED
Viewing direction(Gray inversion)	85/85/85/85

Interface pin

Pin No.	Symbol	Function
1-2	VDD2.8	VDD power input
3	GND	Ground for logic.
4	RESET	Reset signal input terminal, active at 'L'.
5-6	NC	NC
7	GND	Ground for logic.
8	D2N	DATA2- differential data signals.
9	D2P	DATA2+ differential data signals.
10	GND	Ground for logic.
11	D1N	DATA1- differential data signals.
12	D1P	DATA1+ differential data signals.
13	GND	Ground for logic.
14	CLKN	CLK- differential data signals.
15	CLKP	CLK+ differential data signals.
16	GND	Ground for logic.
17	D0N	DATA0- differential data signals.
18	D0P	DATA0+ differential data signals.
19	GND	Ground for logic.
20	D3N	DATA3- differential data signals.
21	D3P	DATA3+ differential data signals.
22	GND	Ground for logic.
23	IOVCC	IO System power supply
24-30	LEDK	Power supply for backlight cathode input terminal.