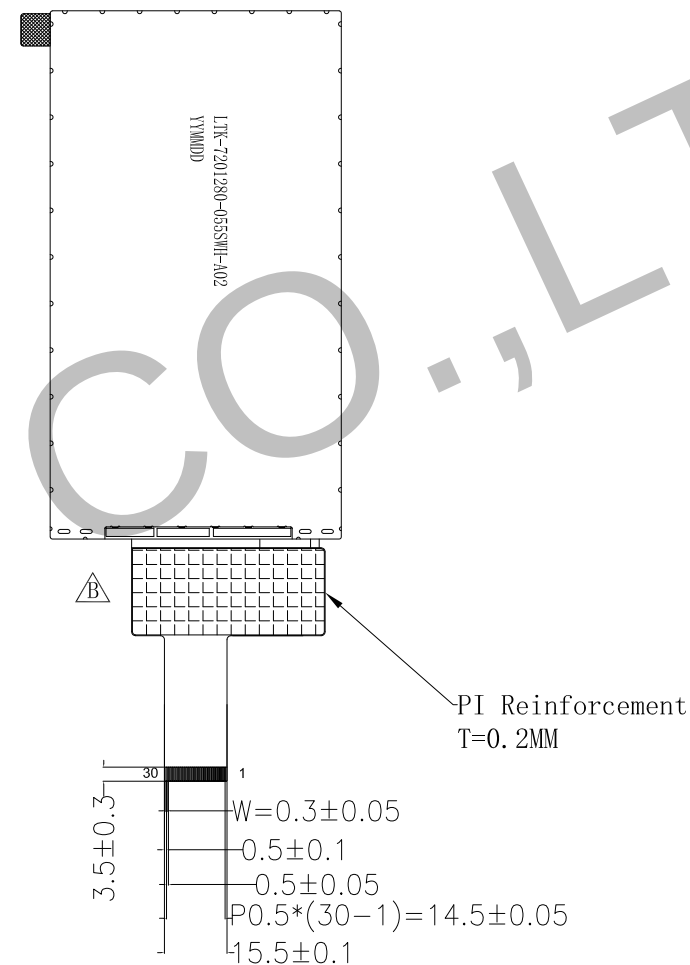
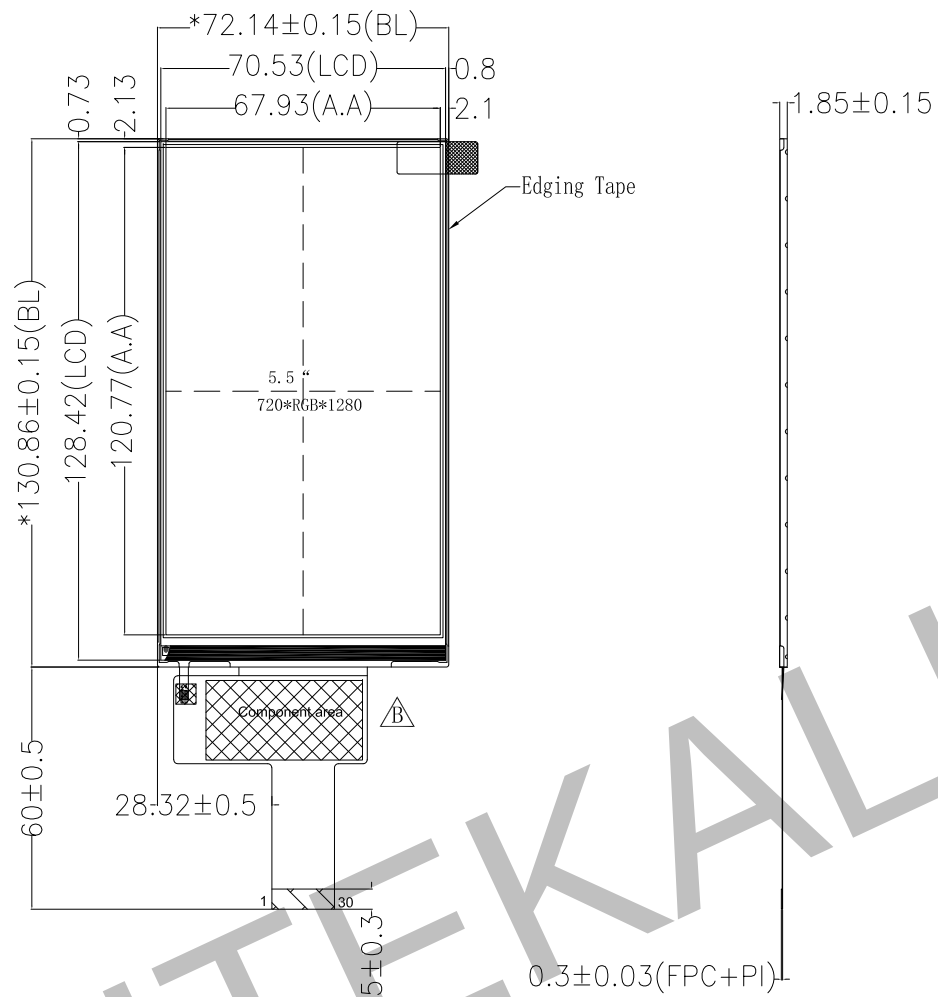




PIN	SYMBOL
1	LEDK
2	NC
3	LEDA
4	NC
5	IOWCC
6	VCI
7	TE
8	RESET
9	LCD_ID
10	NC
11	SPI_SDA
12	SPI_SCL
13	SPI_CSX
14	NC
15	GND
16	D03_P
17	D03_N
18	GND
19	D00_P
20	D00_N
21	GND
22	CK0_P
23	CK0_N
24	GND
25	D01_P
26	D01_N
27	GND
28	D02_P
29	D02_N
30	GND

General specification

Item	specifications	Unit
Active screen size	5 inch Diagonal	-
Display resolution	800x RGB x480	pixel
Module size (W x H x T)	121.0×78.8 ×7.0	mm
Active area (W x H)	108.0x64.8	mm
Pixel pitch (W×H)	0.135x 0.135	mm
Interface	24-bit RGB interface	-
Module weight	TBD±10%	g
Our components and processes are compliant to RoHS standard.		

Item	specifications
LCD type	a-Si TFT
LCD mode	Normally Black
Polarizer mode	Transmissive
Pixel arrangement	RGB-stripe
Backlight type	LED
Viewing direction(Gray inversion)	ALL

Interface pin

Pin No.	Symbol	Function
1	LEDK2	Backlight Cathode Input
2	LEDK1	Backlight Cathode Input
3	LEDA	Backlight Anode Input
4	LEDA	Backlight Anode Input
5	NC	Not Connect
6	GND	Ground
7	VDD-MTP	Power supply for MTP
8	ROM-RLB	SPI Chip Select Pin , leave it open when not used.
9	SDA	Serial communication data input. Normally pull low
10	SCL	Serial communication clock input. Normally pull low
11	LR	Source Right or Left sequence control. Normally pulled high.
12	UD	Gate up or down scan control. Normally pulled low.
13	VCC	Power supply for digital circuits
14	VCC	Power supply for digital circuits
15	GND	Ground
16	DE	Data Input Enable. Active High to enable the data input bus under “DE Mode”. Normally pull low.
17	HSYNC	Horizontal (Line) synchronizing input signal for RGB interface operation
18	VSYNC	Vertical (Frame) synchronizing input signal for RGB interface operation
19	GND	Ground
20	CLK	Clock input for chip internal shift register and control driver
21	GND	Ground
22	B07	Blue Data Input
23	B06	
24	B05	
25	B04	
26	B03	
27	B02	
28	B01	
29	B00	
30	GND	Ground
31	G07	Green Data Input
32	G06	
33	G05	
34	G04	
35	G03	
36	G02	
37	G01	

38	G00	
39	GND	Ground
40	R07	Red Data Input
41	R06	
42	R05	
43	R04	
44	R03	
45	R02	
46	R01	
47	R00	
48	GND	Ground
49	RESET	Global reset pin. Active low to enter Reset State
50	STBYB	Standby mode, Normally pulled high STBYB = “1”, normal operation STBYB = “0” timing controller, source driver will turn off, all output are High-Z