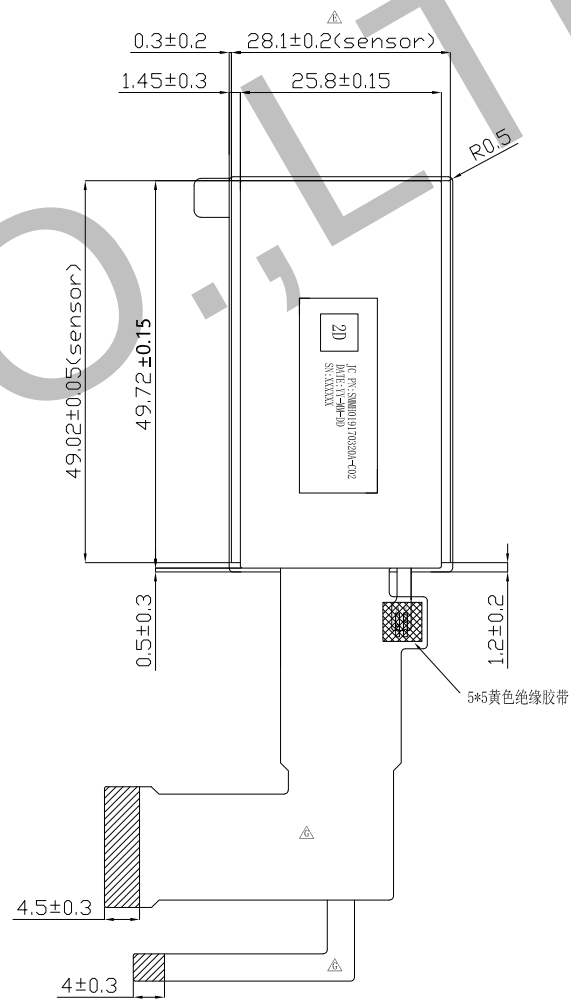
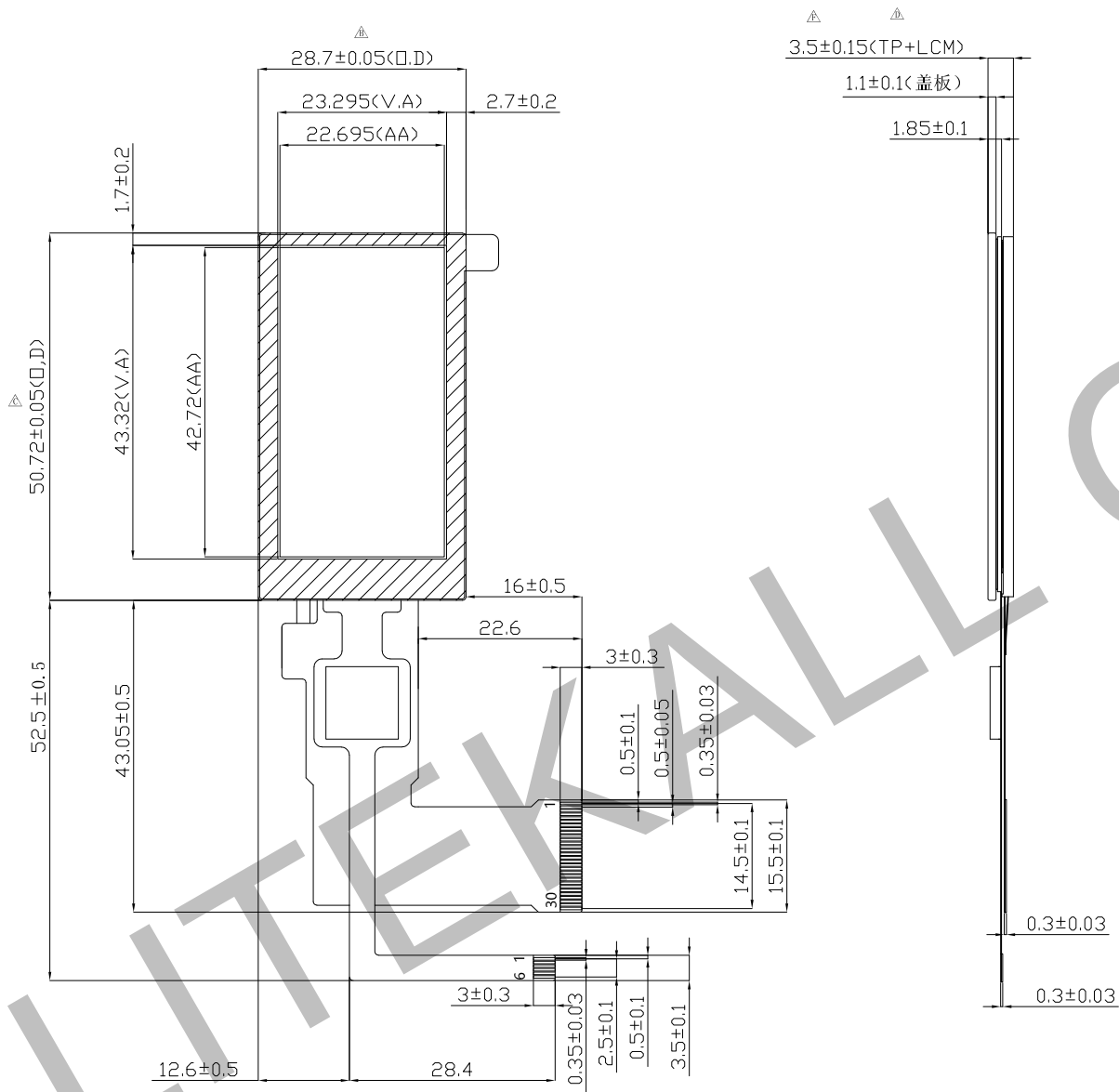




TP接口定義:

PIN	NAME
1	GND
2	SDA
3	SCL
4	INT
5	RESET
6	VDD

PIN	NAME
1	GND
2	VDDI
3	DB17
4	DB16
5	DB15
6	DB14
7	DB13
8	DB11
9	DB10
10	DB9
11	DB8
12	DB7
13	DB6
14	DB5
15	DB4
16	DB3
17	DB2
18	DB1
19	SDA
20	DOTCLK
21	ENABLE
22	HSYNC
23	VSYNC
24	DCX (SCL)
25	CSX
26	RESX
27	VDD
28	GND
29	LEDA
30	LEDK

General specification

Item	specifications	Unit
Active screen size	1.9 inch Diagonal	-
Display resolution	170x RGB x 320	pixel
Module size (W x H x T)	28.7 ×50.72 ×3.9	mm
Active area (W x H)	22.695x42.72	mm
Pixel pitch (W×H)	0.1335 x 0.1335	mm
Interface	SPI+16BIT RGB	-
Module weight	TBD±10%	g
Our components and processes are compliant to RoHS standard.		

Item	specifications
LCD type	a-Si TFT
LCD mode	Normally Black
Polarizer mode	Transmissive
Pixel arrangement	RGB-stripe
Backlight type	LED
Viewing direction(Gray inversion)	ALL

Interface pin

Pin No.	Symbol	Function
1	GND	Ground
2	VDDI	Power Supply for I/O System.
3	DB17	DB[17:0] are used as MCU parallel interface databus. 8-bit I/F: when IM3:0, DB[7:0] are used; when IM3:1, DB[17:10] are used. 9-bit I/F: when IM3:0, DB[8:0] are used; when IM3:1, DB[17:9] are used. 16-bit I/F: when IM3:0, DB[15:0] are used; when IM3:1, DB[17:10] and DB[8:1] are used. 18-bit I/F: DB[17:0] are used. DB[17:0] are used as RGB interface data bus. 6-bit RGB I/F: DB[5:0] are used. 16-bit RGB I/F: DB[17:13], DB[11:1] are used. 18-bit RGB I/F: DB[17:0] are used.
4	DB16	
5	DB15	
6	DB14	
7	DB13	
8	DB11	
9	DB10	
10	DB9	
11	DB8	
12	DB7	
13	DB6	
14	DB5	
15	DB4	
16	DB3	
17	DB2	
18	DB1	
19	SDA	The data is latched on the rising edge of the SCL signal.
20	DOTCLK	Dot clock signal for RGB interface operation.
21	ENABLE	Data enable signal for RGB interface operation.
22	HSYNC	Vertical (Frame) synchronizing input signal for RGB interface operation. If not used, please fix this pin at VDDI or DGND.
23	VSYNC	Data input enable. Active High to enable the data input
24	DCX(SCL)	Serial interface clock.
25	CSX	Chip selection pin
26	RESX	This signal will reset the device and it must be applied to properly initialize the chip. -Signal is active low.
27	VDD	Power Supply
28	GND	Ground
29	LEDA	Power for Backlight
30	LEDK	Power for Backlight