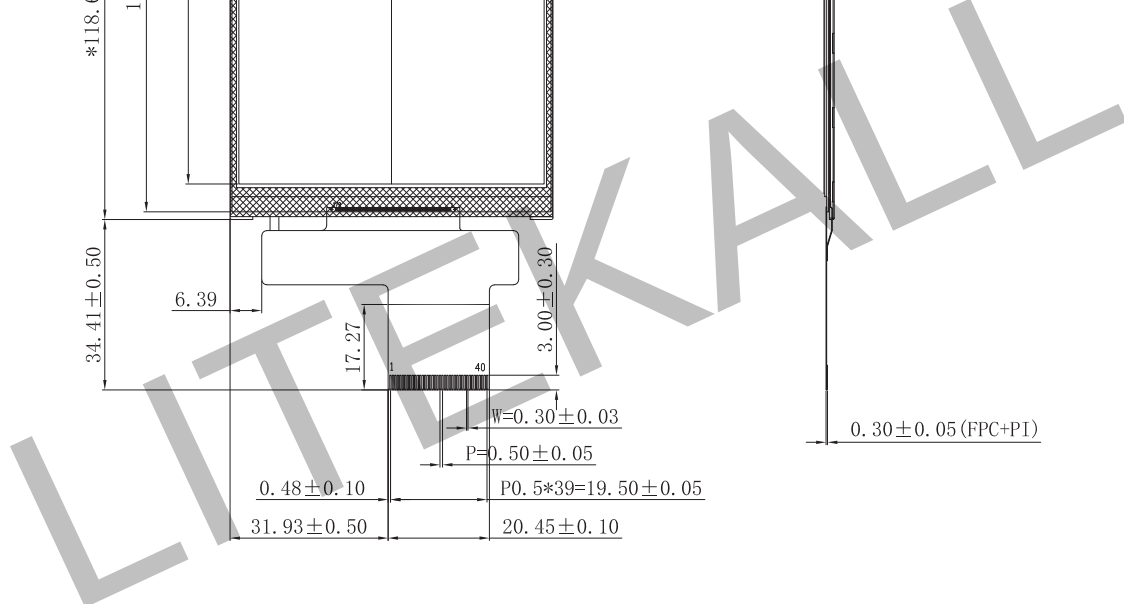


ND.	Assignment
1	LEDK
2	LEDA
3	GND
4	VCC
5	R0
6	R1
7	R2
8	R3
9	R4
10	R5
11	R6
12	R7
13	G0
14	G1
15	G2
16	G3
17	G4
18	G5
19	G6
20	G7
21	B0
22	B1
23	B2
24	B3
25	B4
26	B5
27	B6
28	B7
29	GND
30	PCLK
31	NC
32	HSYNC
33	VSYNC
34	DEN
35	TE
36	GND
37	SCL
38	SDA
39	CS
40	RESET

General specification

Item	specifications	Unit
Active screen size	5.0 inch Diagonal	-
Display resolution	480 x RGB x 854	pixel
Module size (W x H x T)	65.2 × 118.6 × 2.05(Exclude FPC)	mm
Active area (W x H)	61.63 × 109.65	mm
Pixel pitch (W×H)	0.128 x 0.128	mm
Interface	SPI_RGB	-
Module weight	TBD±10%	g
Our components and processes are compliant to RoHS standard.		

Item	specifications
LCD type	a-Si TFT
LCD mode	IPS / Normal Black
Polarizer mode	Transmissive
Pixel arrangement	RGB-stripe
Backlight type	LED
Viewing direction(Gray inversion)	80/80/80/80

Interface pin LCD

Pin Description

Pin No.	Symbol	Function
1	LEDK	LED backlight (Cathode).
2	LEDA	LED backlight (Anode).
3	GND	Ground for logic.
4	VDD	Power supply for voltage
5~12	R0~R7	Red Data.
13~20	G0~G7	Green Data.
21~28	B0~B7	Blue Data.
29	GND	Ground for logic.
30	PCLK	Dot clock signal input. Latching input data at its rising edge.
31	NC	NC
32	HSYNC	Horizontal sync input. Negative polarity.
33	VSYNC	Vertical sync input. Negative polarity.
34	DE	Data enable input. Active high to enable the input data bus.
35	TE	Display single.
36	GND	Ground for logic.
37	SCL	This pin is used serial interface clock in SPI
38	SDI	Serial input signal in SPI I/F.
39	CS	Chip select input pin ("Low" enable).
40	RESET	Reset input pin, Active "L".