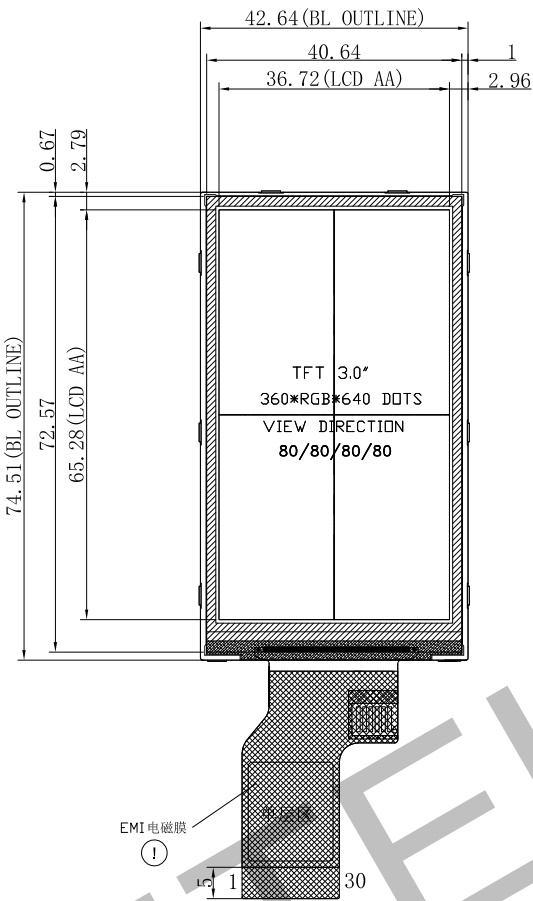


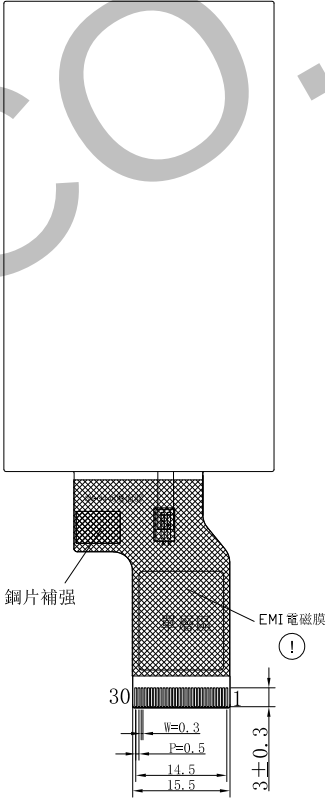
正視圖



側視圖



背視圖



① CTP

1	GND
2	TP_INT
3	TP_SDA
4	TP_SCL
5	TP_RESET
6	TP_VCI

Pin	Interface
01	LEDA
02	LEDK1
03	LEDK2
04	VCI
05	IOVCC
06	RESET
07	TE
08	PWM
09	GND
10	DO_P
11	DO_N
12	GND
13	D1_P
14	D1_N
15	GND
16	CLK_P
17	CLK_N
18	GND
19	NC
20	NC
21	GND
22	NC
23	NC
24	GND
25	TP_INT
26	TP_SDA
27	TP_SCL
28	TP_RESET
29	TP_VCI
30	NC

①

General specification

Item	specifications	Unit
Active screen size	3.0 inch Diagonal	-
Display resolution	360 x RGB x 640	pixel
Module size (W x H x T)	42.64 ×74.51 × 2.25(Exclude FPC)	mm
Active area (W x H)	36.72 × 65.28	mm
Pixel pitch (W×H)	0.102 x 0.102	mm
Interface	MIPI	-
Module weight	TBD±10%	g
Our components and processes are compliant to RoHS standard.		

Item	specifications
LCD type	a-Si TFT
LCD mode	IPS / Normal Black
Polarizer mode	Transmissive
Pixel arrangement	RGB-stripe
Backlight type	LED
Viewing direction(Gray inversion)	ALL

Interface pin

LCD Pin Description

Pin No.	Symbol	Function
1	LEDA	LED Power supply positive
2-3	LEDK	LED Power supply negative
4	VCI	Power supply for analog.
5	IOVCC	Power supply for analog.
6	LCD_RESET	Reset signal input terminal, active at 'L'.
7	TE	For IC Test.
8	PWM	The PWM frequency output for LCD driver control.
9	GND	System ground
10	LCD_D0P	DSI-DATA+ differential data signals.
11	LCD_D0N	DSI-DATA- differential data signals.
12	GND	System ground
13	LCD_D1P	DSI-DATA+ differential data signals.
14	LCD_D1N	DSI-DATA- differential data signals.
15	GND	System ground
16	LCD_CLKP	DSI-CLK+ differential clock signals.
17	LCD_CLK N	DSI-CLK- differential clock signals.
18	GND	System ground
19-20	NC	NC
21	GND	System ground
22-23	NC	NC
24	GND	System ground
25	TP_GND	System power ground.
26	TP_INT	External interrupt to the host
27	TP_SDA	I2C data input and output
28	TP_SCL	I2C clock input
29	TP-RST	External Reset, Low is active
30	TP-VCI	Power supply voltage

TP Pin Description

Pin No.	Symbol	Function
1	TP_GND	System power ground.
2	TP_INT	External interrupt to the host
2	TP_SDA	I2C data input and output
3	TP_SCL	I2C clock input
3	TP-RST	External Reset, Low is active
4	TP-VCI	Power supply voltage