



BL CN:FH28-10S-0.5SH

PIN NO.	SYMBOL
1	A1
2	A2
3	NC
4	K1
5	K2
6	K3
7	NC
8	NC
9	NTC1
10	NTC2

LCM CN :101049-203050

PIN NO.	SYMBOL	PIN NO.	SYMBOL
1	BI5T	16	PIND0
2	DVDD	17	GND
3	DVDD	18	NIND1
4	GND	19	PIND1
5	RESETB	20	GND
6	STBYB	21	NINC
7	GND	22	PINC
8	Fail_dect	23	GND
9	SPI_SDA0	24	NIND2
10	SPI_SDA1	25	PIND2
11	SPI_SCL	26	GND
12	SPI_CSB	27	NIND3
13	NC	28	PIND3
14	GND	29	GND
15	NIND0	30	OTP

General specification

Item	specifications	Unit
Active screen size	9.2 inch Diagonal	-
Display resolution	1920x RGB x 384	pixel
Module size (W x H x T)	238.58 ×58.65 ×6.95	mm
Active area (W x H)	229.248 x45.849	mm
Pixel pitch (W×H)	0.1194 x 0.1194	mm
Interface	RGB	-
Module weight	TBD±10%	g
Our components and processes are compliant to RoHS standard.		

Item	specifications
LCD type	a-Si TFT
LCD mode	IPS / Normal Black
Polarizer mode	Transmissive
Pixel arrangement	RGB-stripe
Backlight type	LED
Viewing direction(Gray inversion)	ALL

Interface pin

Pin No.	Symbol	Function
1	BIST	Normal operation/BIST pattern select. BIST="1": BIST mode. BIST="0": Normal operation.
2	DVDD	Digital power (3.3V)
3	DVDD	Digital power (3.3V)
4	GND	Ground
5	RESETB	Global reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high. (R=10K Ω , C=0.1 μ F)
6	STBYB	Standby mode setting pin. active low. Timing controller, output buffer, DAC and power circuit all of when STBYB is low. Suggest to connecting with an RC reset circuit for stability. Normally pull high. (R=10K Ω , C=1 μ F)
7	GND	Ground
8	Fail_dect	Fail detection signal output normal : Fail_dect output high Fail : Fail_dect output low
9	SPI_SDA _O	Serial interface address and data output for SPI interface
10	SPI_SDAI	Serial interface address and data input for SPI interface
11	SPI_SCL	Serial interface clock input for SPI interface
12	SPI_CSB	Serial interface chip enable signal for SPI interface
13	NC	No connection
14	GND	Digital ground
15	NIND0	Negative LVDS differential data input
16	PIND0	Positive LVDS differential data input
17	GND	Digital ground
18	NIND1	Negative LVDS differential data input
19	PIND1	Positive LVDS differential data input
20	GND	Digital ground
21	NINC	Negative LVDS differential clock input
22	PINC	Positive LVDS differential clock input
23	GND	Digital ground
24	NIND2	Negative LVDS differential data input
25	PIND2	Positive LVDS differential data input
26	GND	Digital ground
27	NIND3	Negative LVDS differential data input
28	PIND3	Positive LVDS differential data input
29	GND	Ground
30	OTP	Power input for OTP programming (8.6V), keep floating when not programming OTP