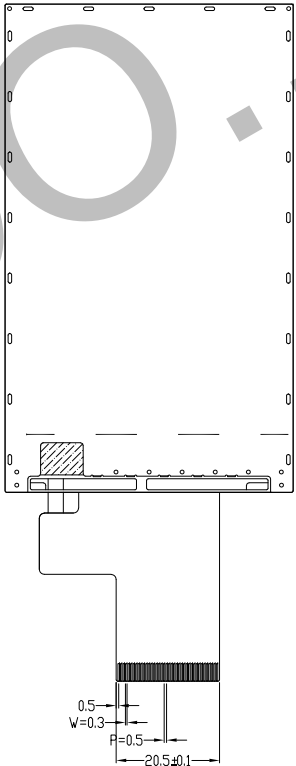


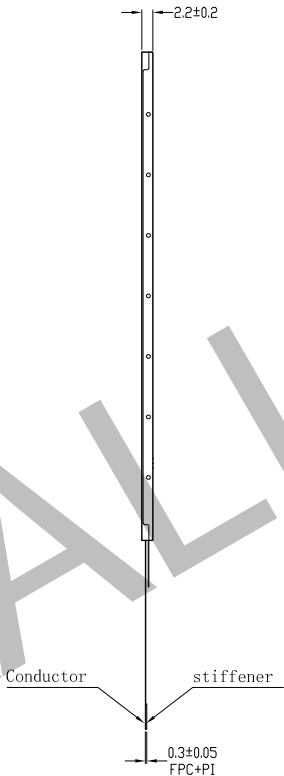
PIN INTERFACE:

| PIN | SYMBOL   |
|-----|----------|
| 1   | VLED-    |
| 2   | VLED+    |
| 3   | GND      |
| 4   | VDD      |
| 5   | R0       |
| 6   | R1       |
| 7   | R2       |
| 8   | R3       |
| 9   | R4       |
| 10  | R5       |
| 11  | R6       |
| 12  | R7       |
| 13  | G0       |
| 14  | G1       |
| 15  | G2       |
| 16  | G3       |
| 17  | G4       |
| 18  | G5       |
| 19  | G6       |
| 20  | G7       |
| 21  | B0       |
| 22  | B1       |
| 23  | B2       |
| 24  | B3       |
| 25  | B4       |
| 26  | B5       |
| 27  | B6       |
| 28  | B7       |
| 29  | GND      |
| 30  | PCLK     |
| 31  | DISP(NC) |
| 32  | HSYNC    |
| 33  | VSYNC    |
| 34  | DE       |
| 35  | NC       |
| 36  | GND      |
| 37  | RESET    |
| 38  | SDA      |
| 39  | SCL      |
| 40  | CS       |

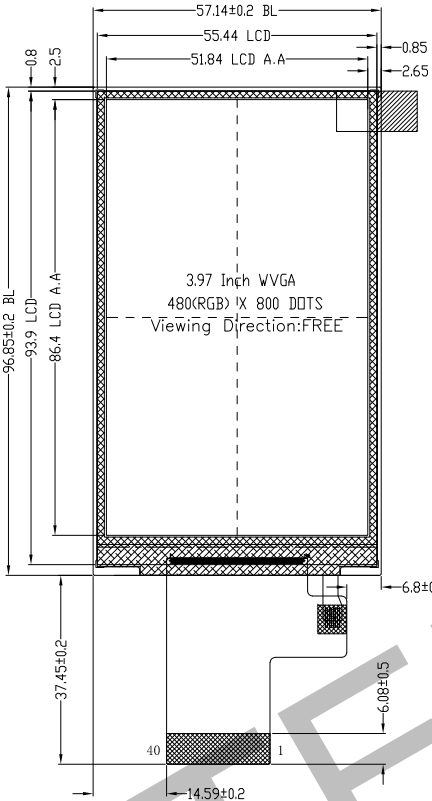
背視圖



側視圖



正視圖



**General specification**

| Item                                                         | specifications                 | Unit  |
|--------------------------------------------------------------|--------------------------------|-------|
| Active screen size                                           | 3.97 inch Diagonal             | -     |
| Display resolution                                           | 480 x RGB x 800                | pixel |
| Module size (W x H x T)                                      | 57.14×96.85 × 2.2(Exclude FPC) | mm    |
| Active area (W x H)                                          | 51.84 × 86.4                   | mm    |
| Pixel pitch ( W×H )                                          | 0.108 x 0.108                  | mm    |
| Interface                                                    | 3SPI_RGB                       | -     |
| Module weight                                                | TBD±10%                        | g     |
| Our components and processes are compliant to RoHS standard. |                                |       |

| Item                              | specifications     |
|-----------------------------------|--------------------|
| LCD type                          | a-Si TFT           |
| LCD mode                          | IPS / Normal Black |
| Polarizer mode                    | Transmissive       |
| Pixel arrangement                 | RGB-stripe         |
| Backlight type                    | LED                |
| Viewing direction(Gray inversion) | 80/80/80/80        |

**Interface pin**

| Pin No. | Symbol  | Function                                                     |
|---------|---------|--------------------------------------------------------------|
| 1       | LEDK    | LED backlight (Cathode).                                     |
| 2       | LEDA    | LED backlight (Anode).                                       |
| 3       | GND     | Ground for logic.                                            |
| 4       | VCI     | VCI power input                                              |
| 5-12    | R0-R7   | Red Data.                                                    |
| 13-20   | G0-G7   | Green Data.                                                  |
| 21-28   | B0-B7   | Blue Data.                                                   |
| 29      | GND     | Ground for logic.                                            |
| 30      | PCLK    | Pixel clock input pin                                        |
| 31      | NC      | NC                                                           |
| 32      | HS      | Horizontal sync signal                                       |
| 33      | VS      | Vertical sync signal                                         |
| 34      | DE      | Data enable input. Active high to enable the input data bus. |
| 35      | NC      | NC                                                           |
| 36      | GND     | Ground for logic.                                            |
| 37      | RESET   | Reset signal input terminal, active at 'L'.                  |
| 38      | SPI_SDA | Serial data input/output bidirectional pin                   |
| 39      | SPI_SCL | Serial clock input                                           |
| 40      | SPI_CS  | A chip select signal                                         |