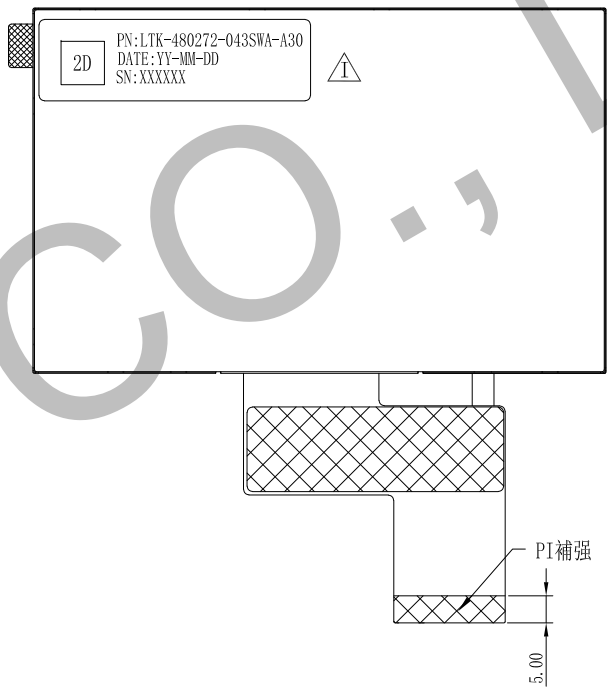
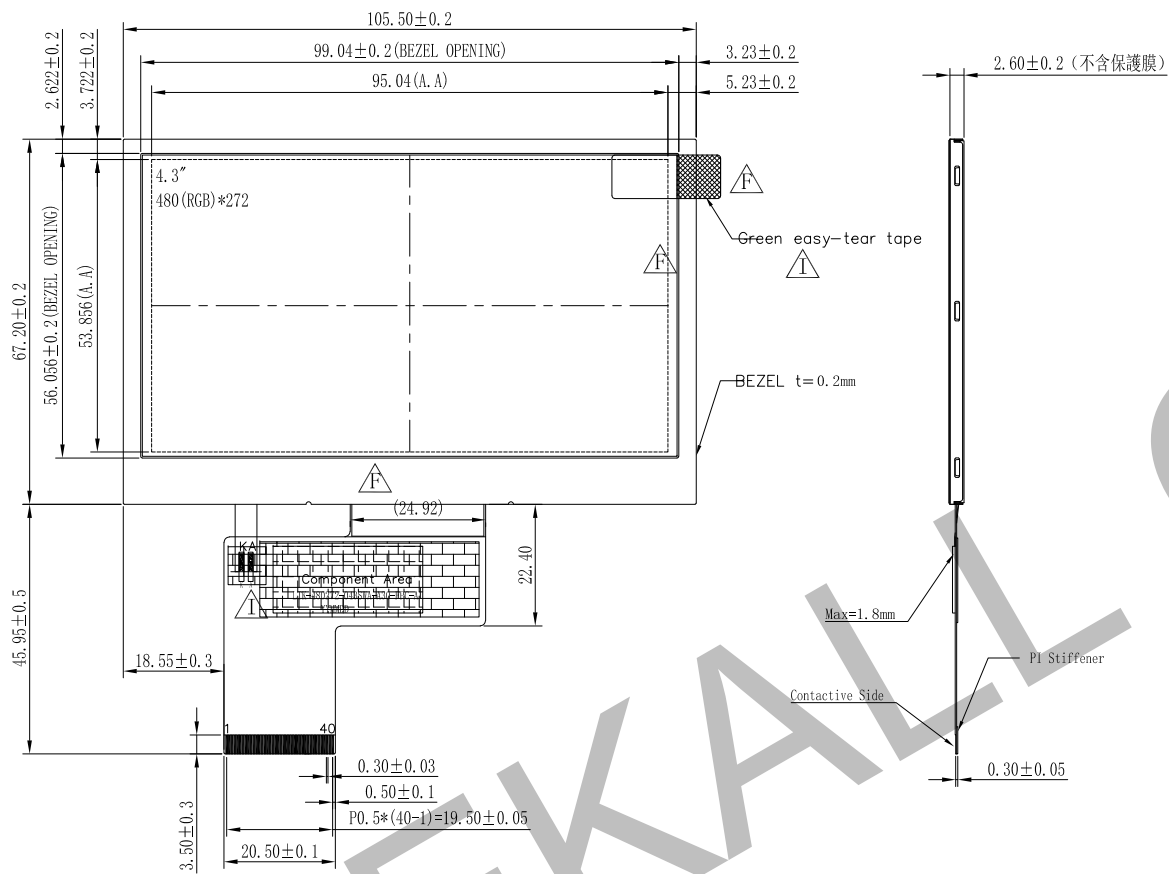




PI補强

高温膠帶

NO.	SYMBOL
Right Pin Assignment	
1	VLED-
2	VLED+
3	GND
4	VDD
5	RO
6	R1
7	R2
8	R3
9	R4
10	R5
11	R6
12	R7
13	G0
14	G1
15	G2
16	G3
17	G4
18	G5
19	G6
20	G7
21	B0
22	B1
23	B2
24	B3
25	B4
26	B5
27	B6
28	B7
29	GND
30	PCLK
31	DISP
32	HSYNC
33	VSYNC
34	DE
35	SCL
36	GND
37	VD1R (UD)
38	HD1R (LR)
39	SDA
40	CS

General specification

Item	specifications	Unit
Active screen size	4.3 inch Diagonal	-
Display resolution	480x RGB x 272	pixel
Module size (W x H x T)	105.5 × 67.2 × 2.60	mm
Active area (W x H)	95.04 x 53.856	mm
Pixel pitch (W×H)	0.198 x 0.198	mm
Interface	RGB	-
Module weight	TBD±10%	g
Our components and processes are compliant to RoHS standard.		

Item	specifications
LCD type	a-Si TFT
LCD mode	IPS / Normally Black
Polarizer mode	Transmissive
Pixel arrangement	RGB-stripe
Backlight type	LED
Viewing direction(Gray inversion)	FREE

Interface pin

Pin No.	Symbol	Function
1	VLED-	Power for Backlight
2	VLED+	Power for Backlight
3	GND	Ground
4	VDD	Power supply.(+3.3V)
5~12	R0~R7	Red data signal
13~20	G0~G7	Green data signal
21~28	B0~B7	Blue data signal
29	GND	Ground for logic.(0V)
30	PCLK	Clock signal for data latching and internal counter of the timing controller.
31	DISP	Display on/off mode control.Internally pulled high. DISP=L,standby mode DISP=H,normally display mode
32	HSYNC	Horizontal sync input with negative polarity.Internally pull high.
33	VSNC	Vertical sync input with negative polarity.Internally pull high.
34	DE	Data input enable. Display access is enabled when DE is “H”.
35	SCL	Serial communication clock input, Internal pull low
36	GND	Ground
37	VDIR(UD)	Vertical scan direction control
38	HDIR(LR)	Horizontal scan direction control
39	SDA	Serial communication data input and output ,Internal pull low.
40	CS	Serial communication chip select, Internal pull high