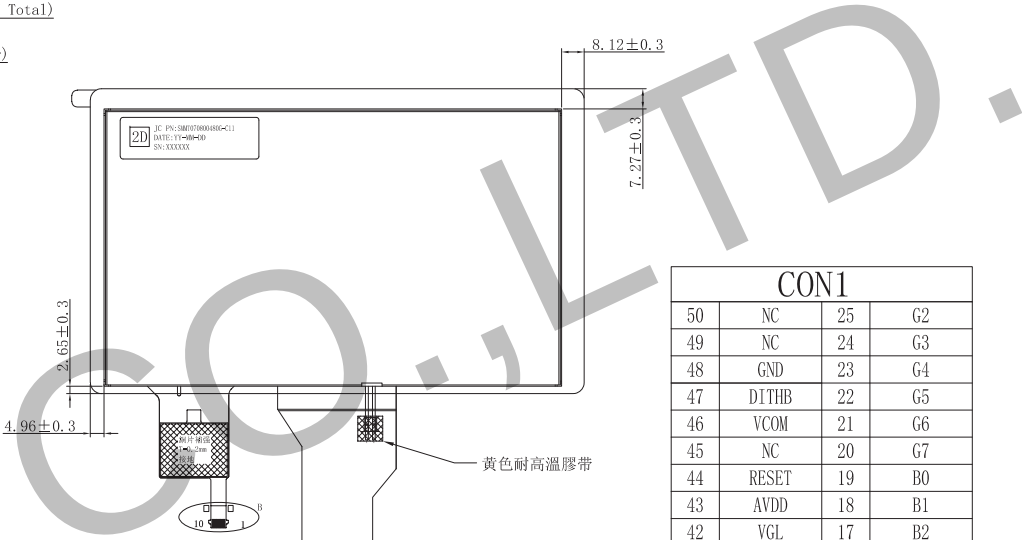
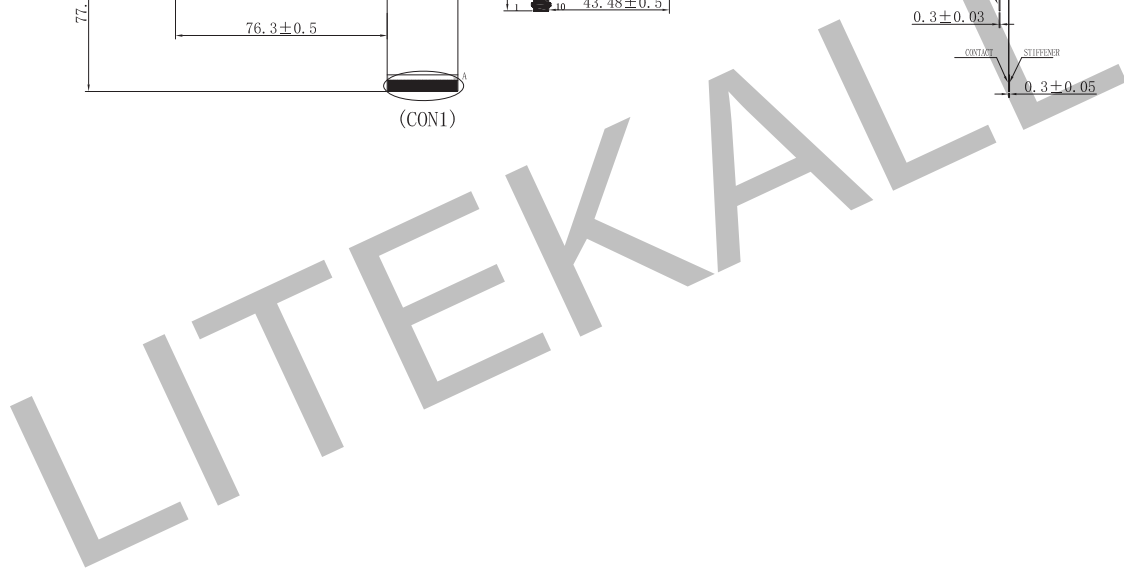




CON1			
50	NC	25	G2
49	NC	24	G3
48	GND	23	G4
47	DITHB	22	G5
46	VCOM	21	G6
45	NC	20	G7
44	RESET	19	B0
43	AVDD	18	B1
42	VGL	17	B2
41	VGH	16	B3
40	U/D	15	B4
39	L/R	14	B5
38	GND	13	B6
37	DCLK	12	B7
36	GND	11	HS
35	R0	10	VS
34	R1	9	DE
33	R2	8	MODE
32	R3	7	DVDD
31	R4	6	VCOM
30	R5	5	GND
29	R6	4	LEDK
28	R7	3	LEDK
27	G0	2	LEDA
26	G1	1	LEDA
NO.	SYMBOL	NO.	SYMBOL

General specification

Item	specifications	Unit
Active screen size	7 inch Diagonal	-
Display resolution	800x RGB x480	pixel
Module size (W x H x T)	178.08×109.92 ×8.1	mm
Active area (W x H)	154.08x85.92	mm
Pixel pitch (W×H)	0.1926x 0.179	mm
Interface	24-bit RGB interface	-
Module weight	TBD±10%	g
Our components and processes are compliant to RoHS standard.		

Item	specifications
LCD type	a-Si TFT
LCD mode	Normally White
Polarizer mode	Transmissive
Pixel arrangement	RGB-stripe
Backlight type	LED
Viewing direction(Gray inversion)	6 O'CLOCK

Interface pin

Pin No.	Symbol	Function
1	LED A	Backlight+
2	LED A	Backlight+
3	LED K	Backlight-
4	LED K	Backlight-
5	GND	Ground
6	VCOM	The power supply for common voltage in TFT driving.
7	DVDD	Power supply for digital circuits.
8	MODE	DE/SYNC mode select. Normally pull high. MODE="1": DE mode. (Default)MODE="0": HSD/VSD mode.
9	DE	Data input enable. Active high to enable the data input bus under "DE Mode ". Normally pull low.
10	VS	Horizontal sync input. Negative polarity. Normally pull high.
11	HS	Vertical sync input. Negative polarity. Normally pull high.
12	B7	Blue Data Input
13	B6	
14	B5	
15	B4	
16	B3	
17	B2	
18	B1	
19	B0	
20	G7	Green Data Input
21	G6	
22	G5	
23	G4	
24	G3	
25	G2	
26	G1	
27	G0	
28	R7	Red Data Input
29	R6	
30	R5	
31	R4	
32	R3	
33	R2	
34	R1	
35	R0	
36	GND	Ground
37	DCLK	Clock for input data. Data latched at rising/falling edge of this signal. Default falling edge. Normally pull low.
38	GND	Ground