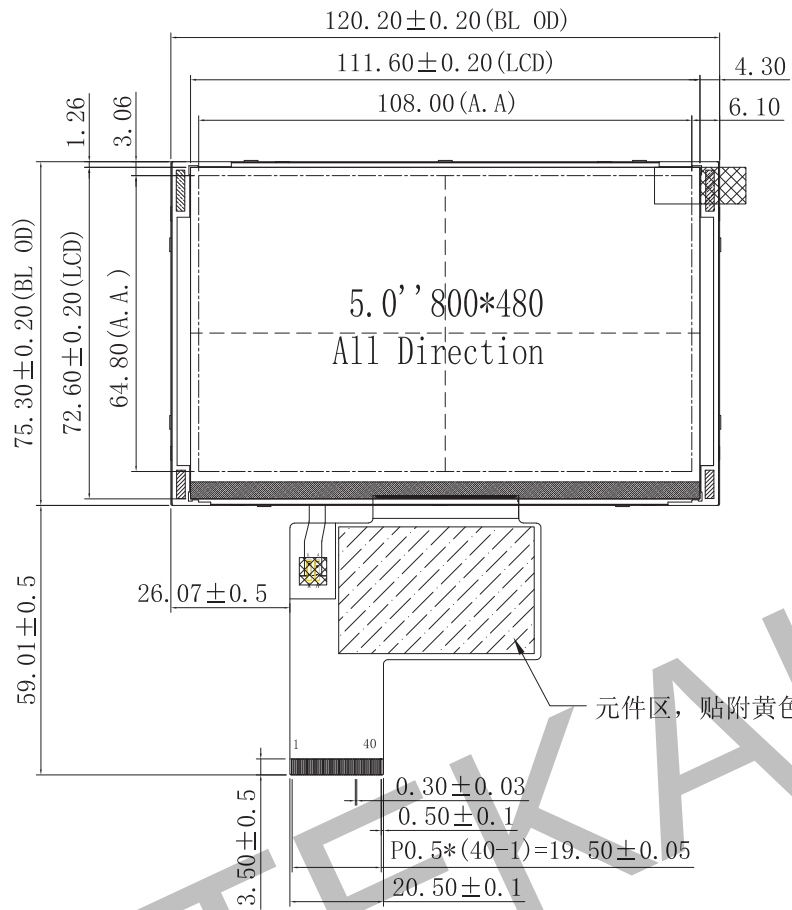
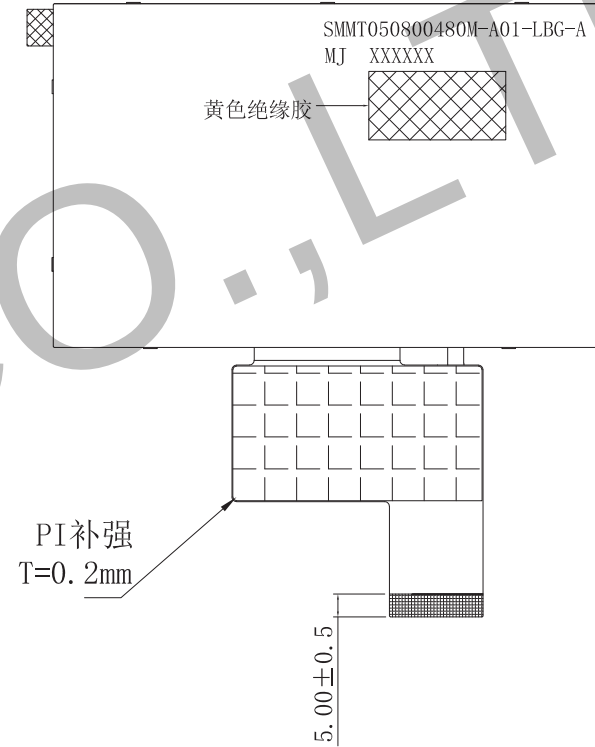
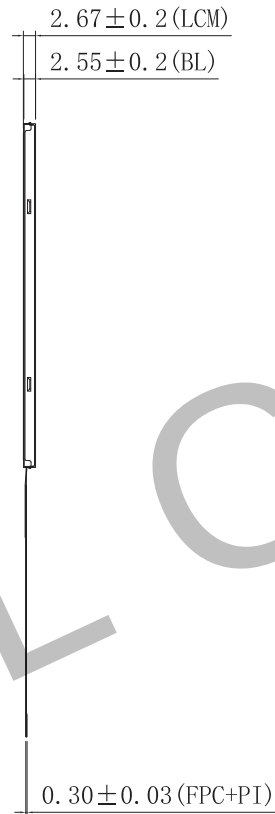




元件区，贴附黄色耐高温胶带



PI补强
T=0.2mm

PIN	SYMBOL
1	LEDK
2	LEDA
3	GND
4	VDD
5	B0
6	R1
7	R2
8	R3
9	R4
10	R5
11	R6
12	R7
13	G0
14	G1
15	G2
16	G3
17	G4
18	G5
19	G6
20	G7
21	B0
22	B1
23	B2
24	B3
25	B4
26	B5
27	B6
28	B7
29	GND
30	DCLK
31	VPP
32	HSTX
33	YSTX
34	DE
35	BLST CON
36	GND
37	SPT_SCL
38	SPT_SDA
39	SPT_CS0
40	RSTB

General specification

Item	specifications	Unit
Active screen size	5.0 inch Diagonal	-
Display resolution	800x RGBW x 480	pixel
Module size (W x H x T)	120.2 ×75.3 ×2.67	mm
Active area (W x H)	108x64.8	mm
Pixel pitch (W×H)	0.135x 0.135	mm
Interface	RGB	-
Module weight	TBD±10%	g
Our components and processes are compliant to RoHS standard.		

Item	specifications
LCD type	a-Si TFT
LCD mode	Normally Black
Polarizer mode	Transmissive
Pixel arrangement	RGB-stripe
Backlight type	LED
Viewing direction(Gray inversion)	ALL

Interface pin

Pin No.	Symbol	Function
1	LEDK	Backlight -
2	LEDA	Backlight +
3	GND	Ground
4	VDD	Power supply
5-12	R0-R7	Red Data Input
13-20	G0-G7	Green Data Input
21-28	B0-B7	Blue Data Input
29	GND	Ground
30	DCLK	Clock input for chip internal shift register and control driver status
31	VPP	VPP Power
32	HSYNC	Horizontal (Line) synchronizing input signal for RGB interface operation
33	VSNC	Vertical (Frame) synchronizing input signal for RGB interface operation
34	DE	Data Input Enable. Active High to enable the data input bus under "DE Mode". Normally pull low
35	BIST_CON	Built In Self Test
36	GND	Ground
37	SPI_SCL	Serial communication clock input. Normally pull low
38	SPI_SDA	Serial communication data input. Normally pull low
39	SPI_CSB	Chip Selection Pin
40	RSTB	Global reset pin. Active low to enter Reset State