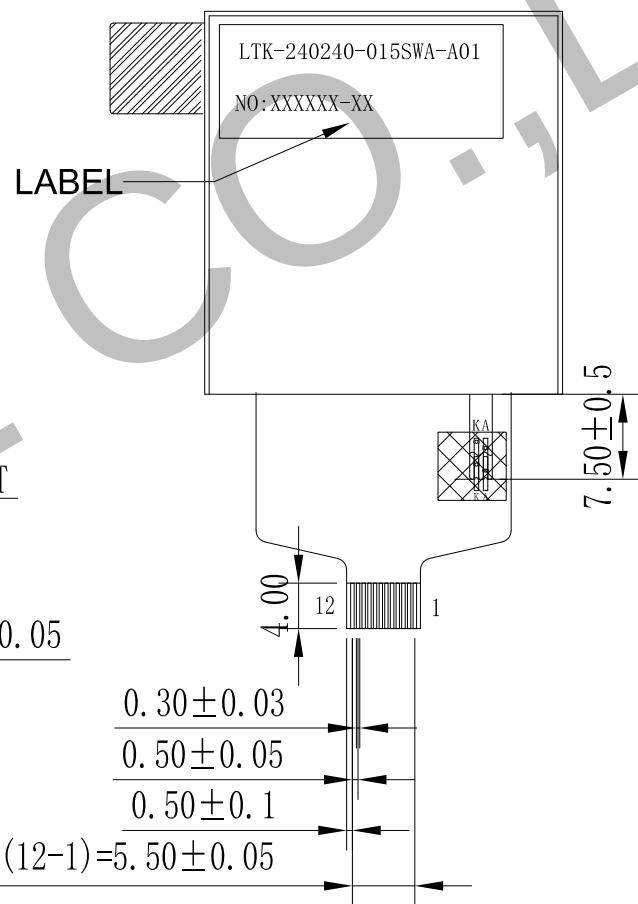
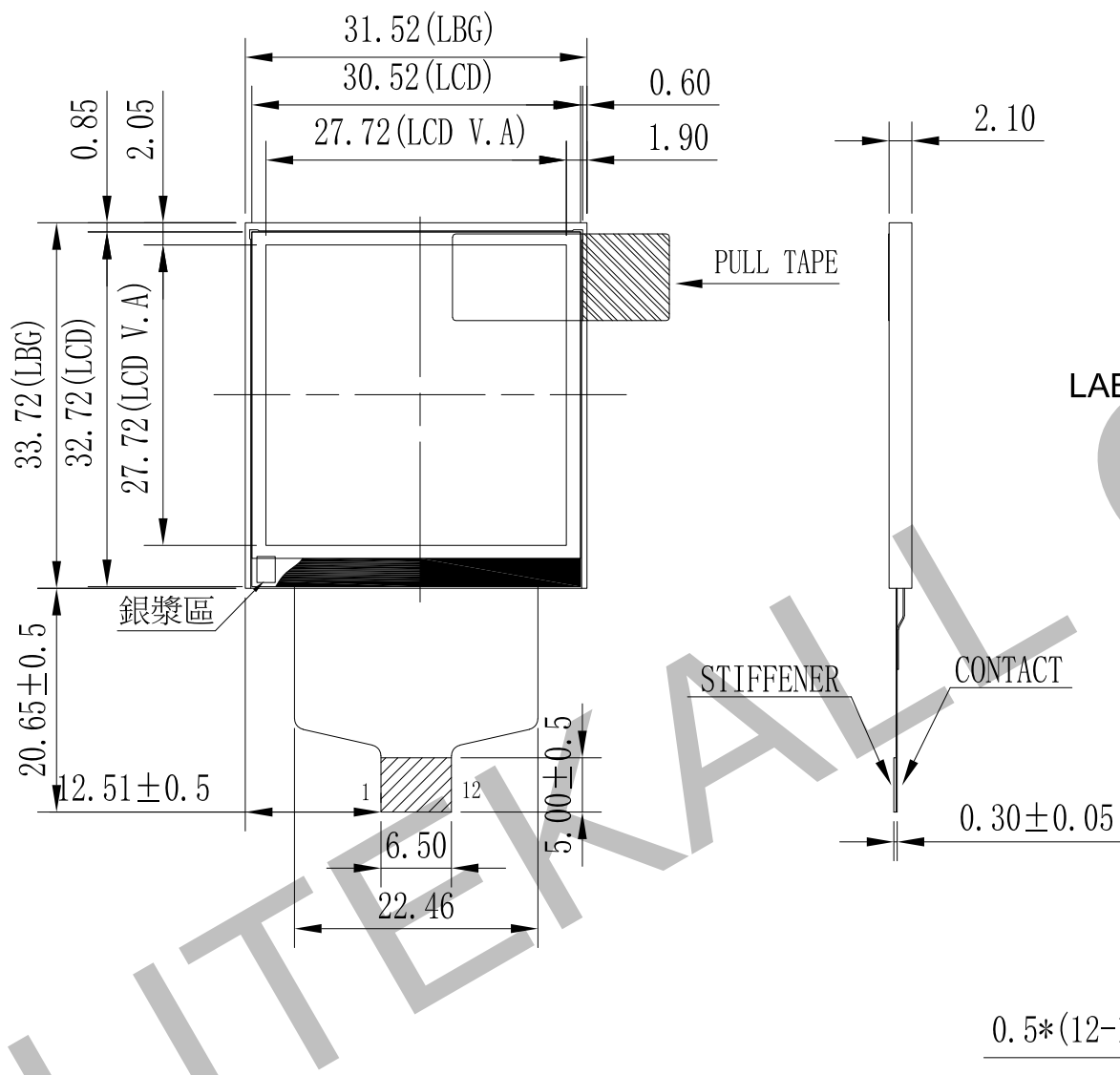




|     |              |
|-----|--------------|
| 12  | GND          |
| 11  | LED A        |
| 10  | LED K        |
| 9   | IOVCC (1.8V) |
| 8   | VDD (2.8V)   |
| 7   | LPTE         |
| 6   | RS           |
| 5   | SDI          |
| 4   | SCLK         |
| 3   | RESET        |
| 2   | CS           |
| 1   | GND          |
| NO. | SYMBOL       |

## General specification

| Item                                                         | specifications     | Unit  |
|--------------------------------------------------------------|--------------------|-------|
| Active screen size                                           | 1.54 inch Diagonal | -     |
| Display resolution                                           | 240x RGB x 240     | pixel |
| Module size (W x H x T)                                      | 31.52 ×33.72 ×2.1  | mm    |
| Active area (W x H)                                          | 27.72x27.72        | mm    |
| Pixel pitch ( W×H )                                          | 0.1155 x 0.1155    | mm    |
| Interface                                                    | 4-LINE SPI         | -     |
| Module weight                                                | TBD±10%            | g     |
| Our components and processes are compliant to RoHS standard. |                    |       |

| Item                              | specifications |
|-----------------------------------|----------------|
| LCD type                          | a-Si TFT       |
| LCD mode                          | Normally Black |
| Polarizer mode                    | Transmissive   |
| Pixel arrangement                 | RGB-stripe     |
| Backlight type                    | LED            |
| Viewing direction(Gray inversion) | ALL            |

### Interface pin

| Pin No. | Symbol     | Function                                                                                 |
|---------|------------|------------------------------------------------------------------------------------------|
| 1       | GND        | Ground                                                                                   |
| 2       | CS         | Chip selection pin                                                                       |
| 3       | RESET      | This signal well reset the device and it must be applied to properly initialize the chip |
| 4       | SCLK       | Serial clock signal                                                                      |
| 5       | SDI        | The data is latched on the rising edge of the SCL signal.                                |
| 6       | RS         | This pin is used as D/CX (data/ command selection)                                       |
| 7       | LPTE       | Tearing effect signal is used to synchronize MCU to frame memory writing.                |
| 8       | VDD(2.8)   | Power Supply for Analog, Digital System and Booster Circuit.                             |
| 9       | IOVDD(1.8) | Power Supply for I/O System.                                                             |
| 10      | LEDK       | Cathode of Backlight                                                                     |
| 11      | LEDA       | Anode of Backlight                                                                       |
| 12      | GND        | Ground                                                                                   |