



General specification

Item	specifications	Unit
Active screen size	10.25 inch Diagonal	-
Display resolution	1920 x RGB x 720	pixel
Module size (W x H x T)	316.15 x 141.88 x 9.42	mm
Active area (W x H)	243.65 x 91.37	mm
Pixel pitch (W×H)	0.1269 x 0.1269	mm
Interface	LVDS	-
Module weight	TBD±10%	g
Our components and processes are compliant to RoHS standard.		

Item	specifications
LCD type	a-Si TFT
LCD mode	IPS / Normal black
Polarizer mode	Transmissive
Pixel arrangement	RGB-stripe
Backlight type	LED
Viewing direction(Gray inversion)	80/80/80/80

Interface pin

Pin No.	Symbol	Function
1	GND	Ground pin for logic circuit and I/O (0V).
2	VDD	Power input for main and I/O power (3.0V to 3.6V).
3	VDD	Power input for main and I/O power (3.0V to 3.7V).
4	NC	No Connect
5	RESET	Global reset pin, active low. If Reset=L, the chip is in reset state.
6	STBYB	Standby mode setting pin, active low. Timing controller, output buffer, DAC and power circuit all off when STBYB is low
7	GND	Ground pin for logic circuit and I/O (0V).
8	OLV0N	Data input pins
9	OLV0P	Data input pins
10	GND	Ground pin for logic circuit and I/O (0V).
11	OLV1N	Data input pins
12	OLV1P	Data input pins
13	GND	Ground pin for logic circuit and I/O (0V).
14	OLV2N	Data input pins
15	OLV2P	Data input pins
16	GND	Ground pin for logic circuit and I/O (0V).
17	OLVCKN	Clk input pins
18	OLVCKP	Clk input pins
19	GND	Ground pin for logic circuit and I/O (0V).
20	OLV3N	Data input pins
21	OLV3P	Data input pins
22	GND	Ground pin for logic circuit and I/O (0V).
23	ELV0N	Data input pins
24	ELV0P	Data input pins
25	GND	Ground pin for logic circuit and I/O (0V).
26	ELV1N	Data input pins
27	ELV1P	Data input pins
28	GND	Ground pin for logic circuit and I/O (0V).
29	ELV2N	Data input pins
30	ELV2P	Data input pins
31	GND	Ground pin for logic circuit and I/O (0V).
32	ELVCLKN	Clk input pins
33	ELVCLKP	Clk input pins
34	GND	Internal regulator output for interface power supply
35	ELV3N	Data input pins

Pin No.	Symbol	Function						
36	ELV3P	Data input pins						
37	GND	Ground pin for logic circuit and I/O (0V).						
38	GND	Ground pin for logic circuit and I/O (1V).						
39	RL	Horizontal shift direction (Source output) selection. <table><tr><th>RL</th><th>Function</th></tr><tr><td>H</td><td>Forward (S1→S2→ ...→ S1920)</td></tr><tr><td>L</td><td>Reverse (S1920→S1919→ ...→ S1)</td></tr></table>	RL	Function	H	Forward (S1→S2→ ...→ S1920)	L	Reverse (S1920→S1919→ ...→ S1)
RL	Function							
H	Forward (S1→S2→ ...→ S1920)							
L	Reverse (S1920→S1919→ ...→ S1)							
40	TB	Vertical shift direction (Gate output) selection. <table><tr><th>TB</th><th>Function</th></tr><tr><td>H</td><td>Forward, Top→Bottom</td></tr><tr><td>L</td><td>Reverse, Bottom→Top</td></tr></table>	TB	Function	H	Forward, Top→Bottom	L	Reverse, Bottom→Top
TB	Function							
H	Forward, Top→Bottom							
L	Reverse, Bottom→Top							
41	NC	Ground.						
42	NC(FAIL_DET)	Data input pins						
43	NC	No Connect						
44	VDD	Power input for main and I/O power (3.0V to 3.6V).						
45	NC(BIST)	Keep floating, internal used for LCM maker						
46	NC(SDA)	Keep floating, internal used for LCM maker						
47	NC(SCL)	Keep floating, internal used for LCM maker						
48	NC(CS)	Keep floating, internal used for LCM maker						
49	NC	No Connect						
50	NC(VDD_OTP)	Keep floating, internal used for LCM maker						