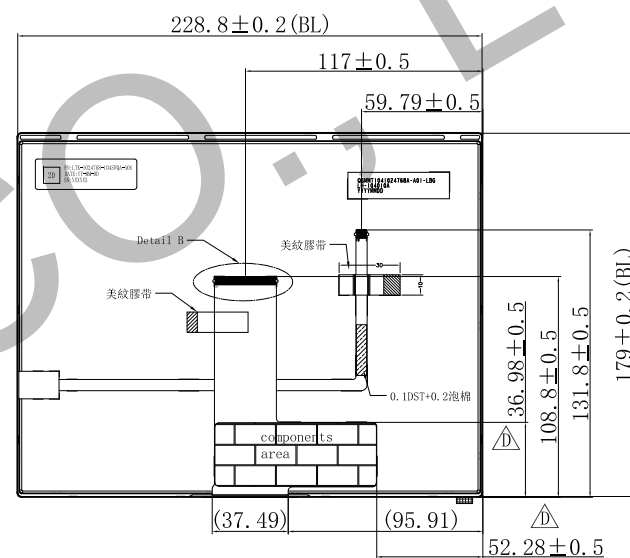
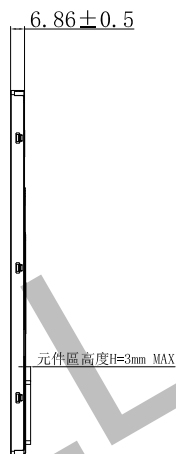




TPT PIN	DEFINITION
1	GND
2	NC
3	VDD
4	GND
5	NC
6	VDD
7	GND
8	NC
9	NC
10	SPI-SDA
11	SPI-SCL
12	SPI-CS
14	NC
15	GND
16	VDD
18	LVDS_D3P
19	LVDS_D3N
20	GND
21	LVDS_CK_P
22	LVDS_CK_N
23	GND
24	LVDS_D2P
25	LVDS_D2N
26	GND
27	LVDS_D1P
28	LVDS_D1N
29	GND
30	LVDS_D0P
31	LVDS_D0N
32	GND
33	GND
34	GRB
35	STRVB
36	SHLR
37	VDD
38	LPDN
39	GND
40	NC
41	NC
42	NC
43	GND
44	VDD
45	GND
46	NC
47	NC
48	VPP
49	NC
50	NC
51	NC
52	NC
53	GND
54	VDD
55	SELB
56	NC
57	VDD
58	NC
59	GND
60	GND

PTN No.	SYMBOL	52	NC
1	LEDA	53	NC
2	LEDA	54	GND
3	LEDA	55	VDD
4	NC	56	SELB
5	NC	57	NC
6	NC	58	VDD
7	LEDK1	59	NC
8	LEDK2	60	GND
9	LEDK3		
10	LEDK4		

General specification

Item	specifications	Unit
Active screen size	10.4 inch Diagonal	-
Display resolution	1024 x RGB x 768	pixel
Module size (W x H x T)	229.6 x 179.8 x 6.86	mm
Active area (W x H)	211.2 x 158.4	mm
Pixel pitch (W×H)	0.2063 x 0.2063	mm
Interface	4-port LVDS	-
Module weight	TBD±10%	g
Our components and processes are compliant to RoHS standard.		

Item	specifications
LCD type	a-Si TFT
LCD mode	IPS / Normal black
Polarizer mode	Transmissive
Pixel arrangement	RGB-stripe
Backlight type	LED
Viewing direction(Gray inversion)	85/85/85/85

Interface pin

Pin No.	Symbol	Function
1	GND	System ground
2	NC	No connection
3	VDD	Power supply
4	GND	System ground
5	NC	No connection
6	VDD	Power supply
7	GND	System ground
8~14	NC	No connection
15	GND	System ground
16	VDD	Power supply
17	GND	System ground
18	PIND3	Input pin for LVDS display data
19	NIND3	Input pin for LVDS display data
20	GND	System ground
21	PINC	Input pin for LVDS display data
22	INNC	Input pin for LVDS display data
23	GND	System ground
24	PIND2	Input pin for LVDS display data
25	NIND2	Input pin for LVDS display data
26	GND	System ground
27	PIND1	Input pin for LVDS display data
28	NIND1	Input pin for LVDS display data
29	GND	System ground
30	PIND0	Input pin for LVDS display data
31	NIND0	Input pin for LVDS display data
32	GND	System ground
33	GND	System ground
34	GRB	RESET control
35	STBYB	STBYB control
36	SHLR	Source Right or Left sequence control
37	VDD	Power supply
38	UPDN	Gate up or down scan control
39	GND	System ground
40~41	NC	No connection
42	NC(DITH)	No connection (The DITH pin is enabled inside the IC)
43	GND	System ground
44	VDD	Power supply
45	GND	System ground

Pin No.	Symbol	Function
46~52	NC	No connection
53	GND	System ground
54	VDD	Power supply
55	SELB	lane mode Control
56	NC	No connection
57	VDD	Power supply
58	NC	No connection
59	GND	System ground
60	GND	System ground