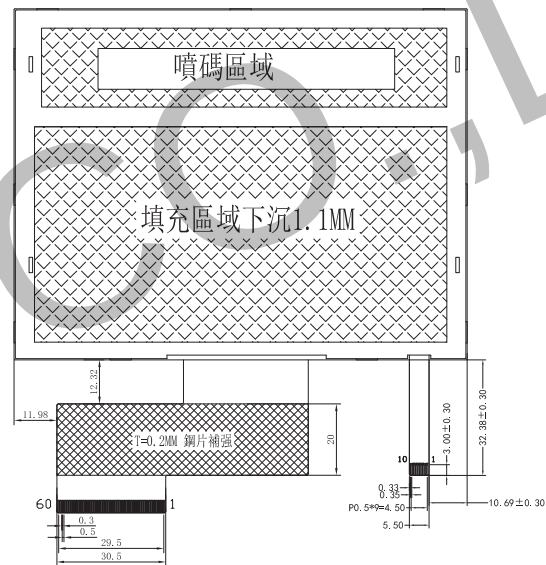
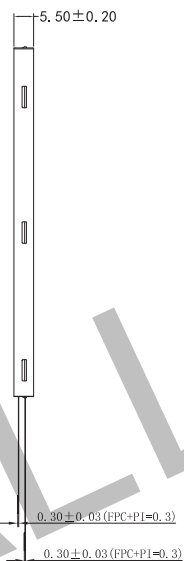
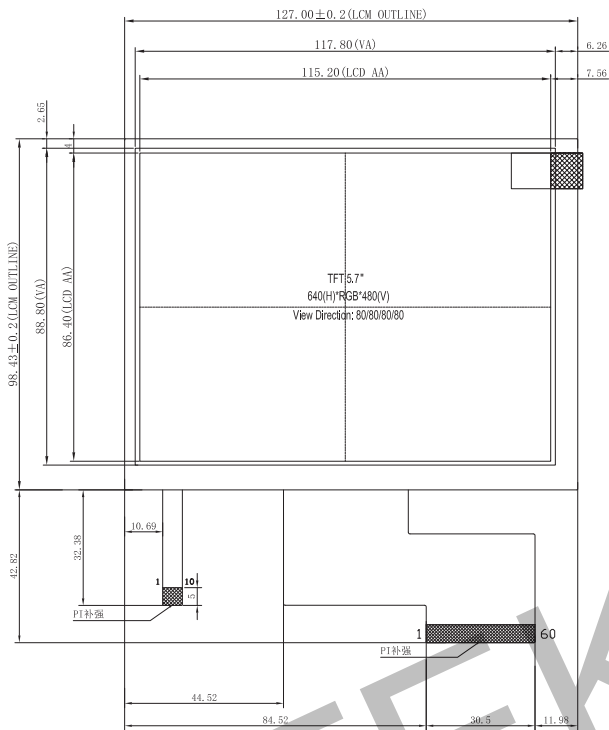




PIN INTERFACE:

PIN	SYMBOL
1	SDA
2	CSX
3	SCL
4	RESX
5-12	B0-B7
13-20	G0-G7
21-28	R0-R7
29	GS
30	SS
31	LAN0
32	LAN1
33	LVFMT
34	STBYB
35	IM1
36	IM0
37	DCLK
38	DE
39	VS
40	HS
41	DS2
42	DS1
43	DS0
44	PS
45	GND
46	DON
47	D0P
48	D1N
49	D1P
50	CKN
51	CKP
52	D2N
53	D2P
54	D3N
55	D3P
56	IOVCC
57	AVDD
58	AVEE
59	VPP
60	GND

General specification

Item	specifications	Unit
Active screen size	5.7 inch Diagonal	-
Display resolution	640 x RGB x 480	pixel
Module size (W x H x T)	127.00 × 98.43 × 5.5(Exclude FPC)	mm
Active area (W x H)	115.20 × 86.4	mm
Pixel pitch (W×H)	0.18 x 0.18	mm
Interface	RGB_24BIT	-
Module weight	TBD±10%	g
Our components and processes are compliant to RoHS standard.		

Item	specifications
LCD type	a-Si TFT
LCD mode	IPS / Normal Black
Polarizer mode	Transmissive
Pixel arrangement	RGB-stripe
Backlight type	LED
Viewing direction(Gray inversion)	80/80/80/80

Interface pin

Description

Pin No.	Symbol	Function
1	SDA	SPI Data
2	CSX	Chip Select
3	SCL	SPI clock
4	RESX	Reset
5~12	B0~B7	RGB data
13~20	G0~G7	RGB data
21~28	R0~R7	RGB data
29	GS	gate scan
30	SS	source scan
31	LAN0	lane mode
32	LAN1	lane mode
33	LVFMT	LVDS data for mat
34	STBYB	standby mode
35	IM1	IF mode select
36	IM0	IF mode select
37	DCLK	RGB clock
38	DE	RGB data enable
39	VS	RGB V-sync
40	HS	RGB H-sync
41	DS2	data lane seq
42	DS1	data lane seq
43	DS0	data lane seq
44	PS	data lane seq
45	GND	Ground
46	D0N	LVDS D0N
47	D0P	LVDS D0P
48	D1N	LVDS D1N
49	D1P	LVDS D1P
50	CKN	LVDS CKN
51	CKP	LVDS CKP
52	D2N	LVDS D2N
53	D2P	LVDS D2P
54	D3N	LVDS D3N
55	D3P	LVDS D3P
56	IOVCC	Power
57	AVDD	Power
58	AVEE	Power
59	VPP	OTP Power
60	GND	Ground