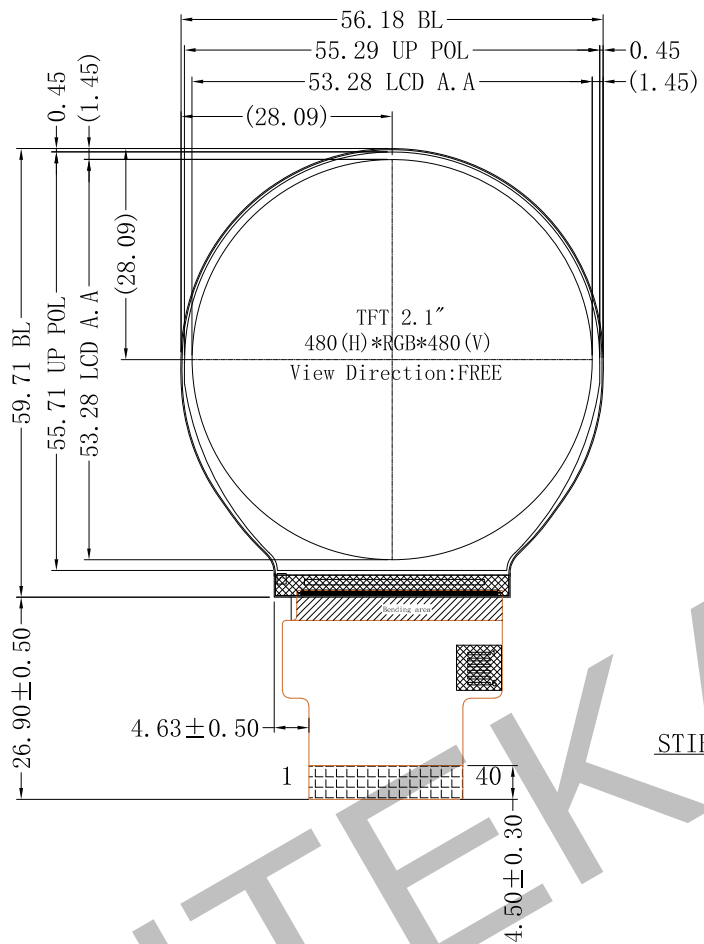
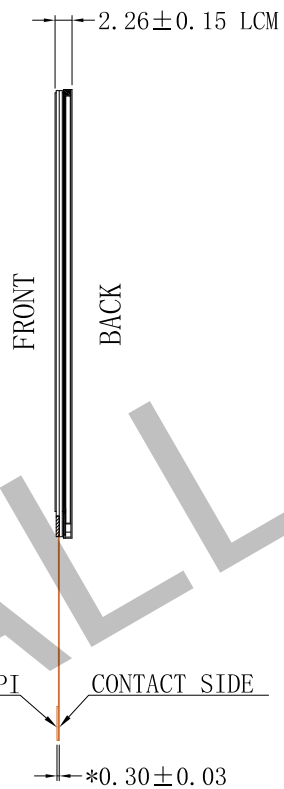


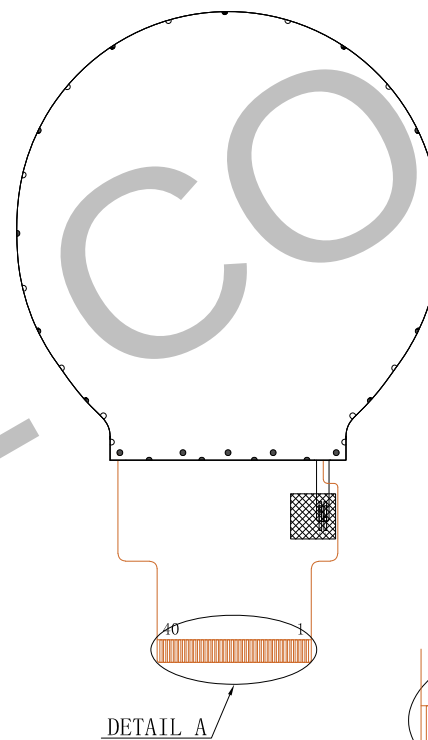
正視圖



側視圖



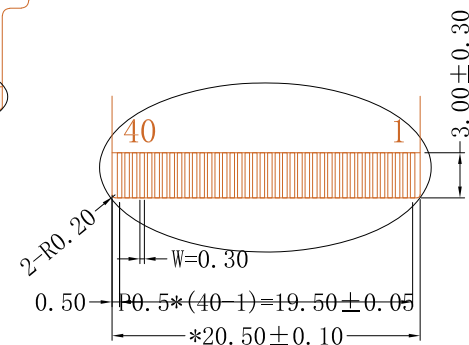
背視圖



TP INTERFACE

|   |        |
|---|--------|
| 1 | VDD    |
| 2 | TP_SCL |
| 3 | TP_SDA |
| 4 | TP_INT |
| 5 | TP_RST |
| 6 | GND    |

| Pin   | Interface |
|-------|-----------|
| 01    | LEDA      |
| 02    | LEDK      |
| 03    | LEDK      |
| 04    | GND       |
| 05    | VCI       |
| 06    | RESET     |
| 07    | NC        |
| 08    | NC        |
| 09    | SDA       |
| 10    | SCK       |
| 11    | CS        |
| 12    | PCLK      |
| 13    | DE        |
| 14    | VSYN      |
| 15    | HSYN      |
| 16-33 | DB0-DB17  |
| 34    | GND       |
| 35    | TP_INT    |
| 36    | TP_SDA    |
| 37    | TP_SCL    |
| 38    | TP_RESET  |
| 39    | TP_VCI    |
| 40    | GND       |



DETAIL A (2:1)

## General specification

| Item                                                         | specifications                   | Unit  |
|--------------------------------------------------------------|----------------------------------|-------|
| Active screen size                                           | 2.1 inch Diagonal                | -     |
| Display resolution                                           | 480 x RGB x 480                  | pixel |
| Module size (W x H x T)                                      | 56.18 ×59.71 × 2.26(Exclude FPC) | mm    |
| Active area (W x H)                                          | 53.28 × 53.28                    | mm    |
| Pixel pitch ( W×H )                                          | 0.111 x 0.111                    | mm    |
| Interface                                                    | 3SPI_18BIT                       | -     |
| Module weight                                                | TBD±10%                          | g     |
| Our components and processes are compliant to RoHS standard. |                                  |       |

| Item                              | specifications     |
|-----------------------------------|--------------------|
| LCD type                          | a-Si TFT           |
| LCD mode                          | IPS / Normal Black |
| Polarizer mode                    | Transmissive       |
| Pixel arrangement                 | RGB-stripe         |
| Backlight type                    | LED                |
| Viewing direction(Gray inversion) | 80/80/80/80        |

## Interface pin

| Pin No. | Symbol   | Function                                                                                         |
|---------|----------|--------------------------------------------------------------------------------------------------|
| 1       | LED A    | LED ANODE                                                                                        |
| 2       | LED K    | LED CATHODE                                                                                      |
| 3       | LED K    | LED CATHODE                                                                                      |
| 4       | GND      | Ground for logic.                                                                                |
| 5       | VCI      | VCI power input                                                                                  |
| 6       | RESET    | Reset signal input terminal, active at 'L'.                                                      |
| 7       | NC       | NC                                                                                               |
| 8       | NC       | NC                                                                                               |
| 9       | SDA      | SPI Data signal                                                                                  |
| 10      | SCK      | SPI Clock signal                                                                                 |
| 11      | CS       | SPI Chip select signal                                                                           |
| 12      | PLCK     | RGB dot clock signal                                                                             |
| 13      | DE       | RGB data enable signal                                                                           |
| 14      | VSYNC    | RGB frame synchronizing signal                                                                   |
| 15      | HSYNC    | RGB line synchronizing signal                                                                    |
| 16-33   | DB0-DB17 | RGB data signal (DB0:BIU LSB;DB5:BIU MSB;DB6:GREEN LSB;DB11:GREEN,MSB;DB12:RED LSB;DB17:RED MSB) |
| 34      | GND      | Ground for logic.                                                                                |
| 35      | TP_Int   | Touch Interrupt                                                                                  |
| 36      | TP_SDA   | Touch IIC Data signal                                                                            |
| 37      | TP_SCL   | Touch IIC Clock signal                                                                           |
| 38      | TP-RESET | Touch Reset Signal                                                                               |
| 39      | TP-V CI  | Touch Power supply                                                                               |
| 40      | GND      | Ground for logic.                                                                                |