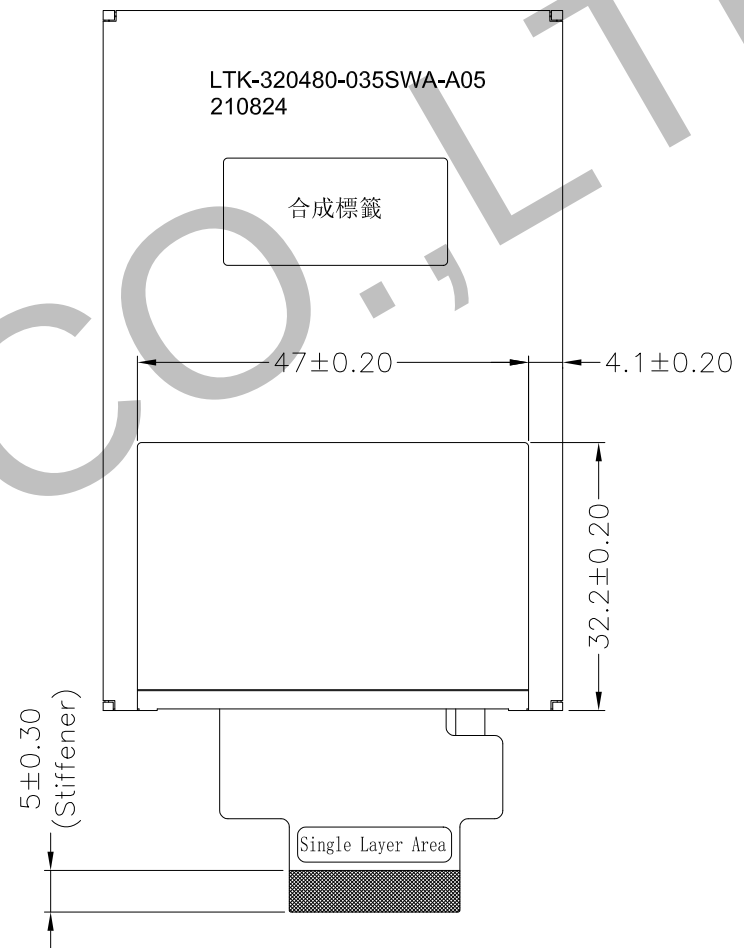
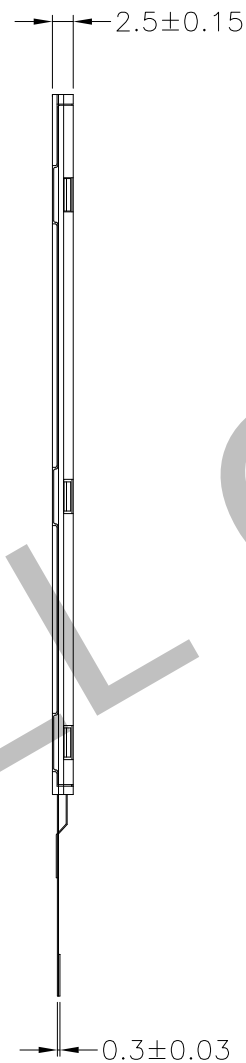
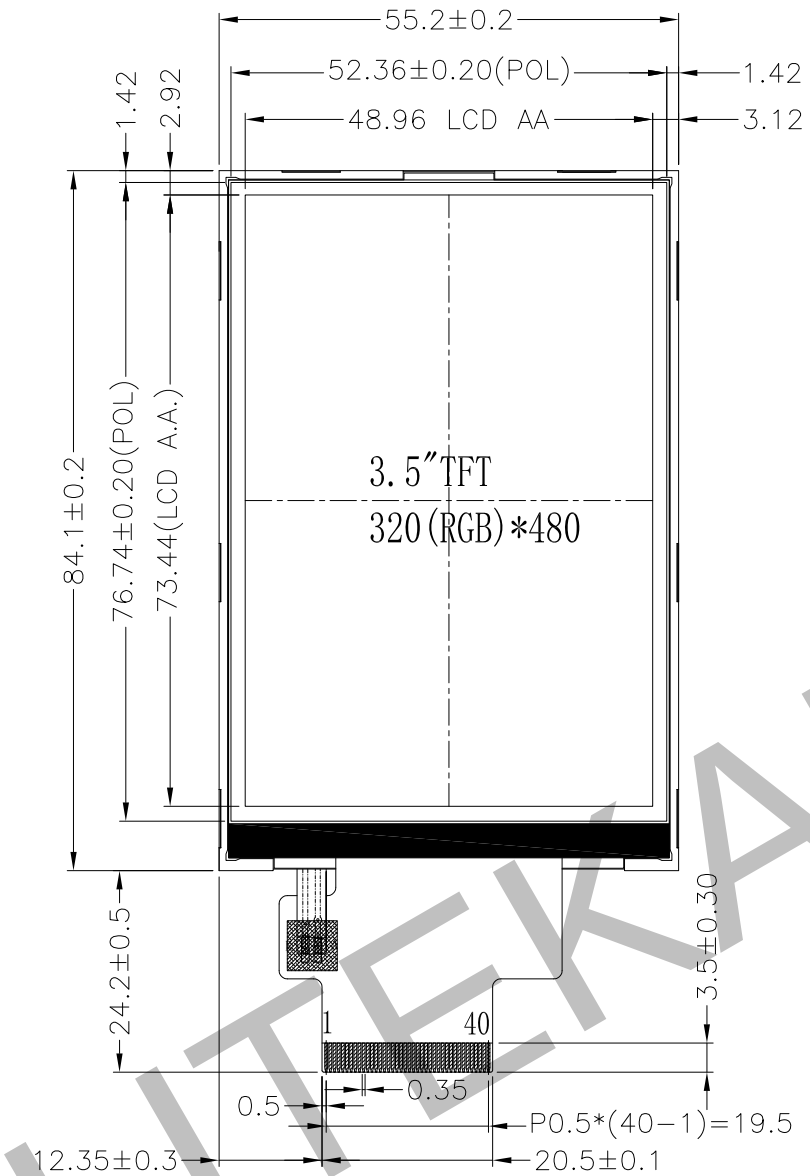




PIN	SYMBOL
1	GND
2	LEDK
3	LEDA
4	VDDA
5	VDDI
6	TE
7	CSX
8	DCX(RS)
9	WRX/SCL
10	RDX
11	SDA
12	DOUT/SDO
13	DB0
14	DB1
15	DB2
16	DB3
17	DB4
18	DB5
19	DB6
20	DB7
21	DB8
22	DB9
23	DB10
24	DB11
25	DB12
26	DB13
27	DB14
28	DB15
29	DB16
30	DB17
31	ENABLE
32	DCLK
33	HSYNC
34	VSYSN
35	RESET
36	IM2
37	IM1
38	IM0
39	GND
40	NC

General specification

Item	specifications	Unit
Active screen size	3.5 inch Diagonal	-
Display resolution	320x RGB x 480	pixel
Module size (W x H x T)	55.2 ×84.1 ×2.5	mm
Active area (W x H)	48.96x73.44	mm
Pixel pitch (W×H)	0.153x 0.153	mm
Interface	MCU 18Bit/4-SPI+RGB18Bit	-
Module weight	TBD±10%	g
Our components and processes are compliant to RoHS standard.		

Item	specifications
LCD type	a-Si TFT
LCD mode	Normally White
Polarizer mode	Transmissive
Pixel arrangement	RGB-stripe
Backlight type	LED
Viewing direction(Gray inversion)	6 O'CLOCK

Interface pin

Pin No.	Symbol	Function
1	GND	Power Ground.
2	LEDK	Cathode of Backlight
3	LEDA	Anode of Backlight
4	VDDA	Supply voltage to the analog circuit
5	VDDI	Supply voltage to the interface pins
6	TE	Tearing effect signal, If not used. leave this pin open.
7	CSX	Chip select input signal
8	DCX(RS)	Display data/command selection (RS) pin in MCU interface. DCX=1: display data or parameter. DCX=0: register index / command.
9	WRX/SCL	Write enable in MCU parallel interface. In SPI mode, this pin is used as SCL.
10	RDX	Read data signal
11	SDA	SPI interface input/output pin. The data is latched on the rising edge of the SCL signal. If not used, please fix this pin at VDDI or GND level.
12	DOUT/SDO	SPI interface output pin. The data is outputted on the falling edge of the SCL signal. If not used, please fix this pin at floating.
13~30	DB0~DB17	Data BUS
31	ENABLE	Data enable signal for RGB interface operation. If not used, please fix this pin at VDDI or GND.
32	DCLK	Dot clock signal for RGB interface operation. If not used, please fix this pin at VDDI or GND.
33	HSYNC	Horizontal synchronizing input signal for RGB interface operation. If not used, please fix this pin at VDDI or GND.
34	VSYNC	Vertical synchronizing input signal for RGB interface operation. If not used, please fix this pin at VDDI or GND.
35	RESET	Reset signal
36	IM2	Select the interface mode Note1
37	IM1	
38	IM0	
39	GND	Ground
40	NC	No Connect