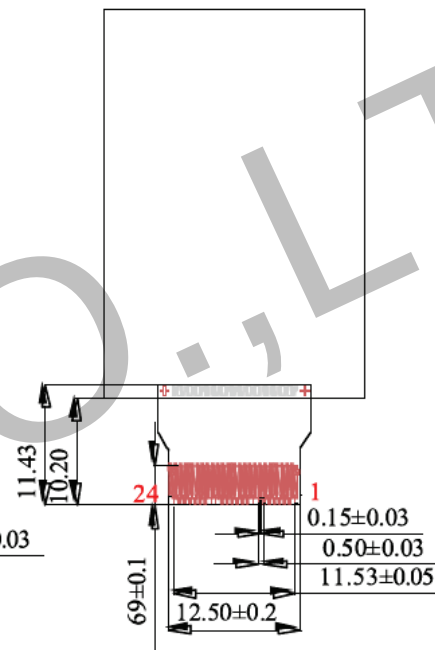
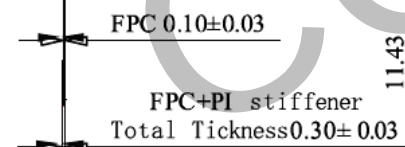
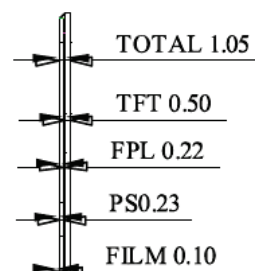
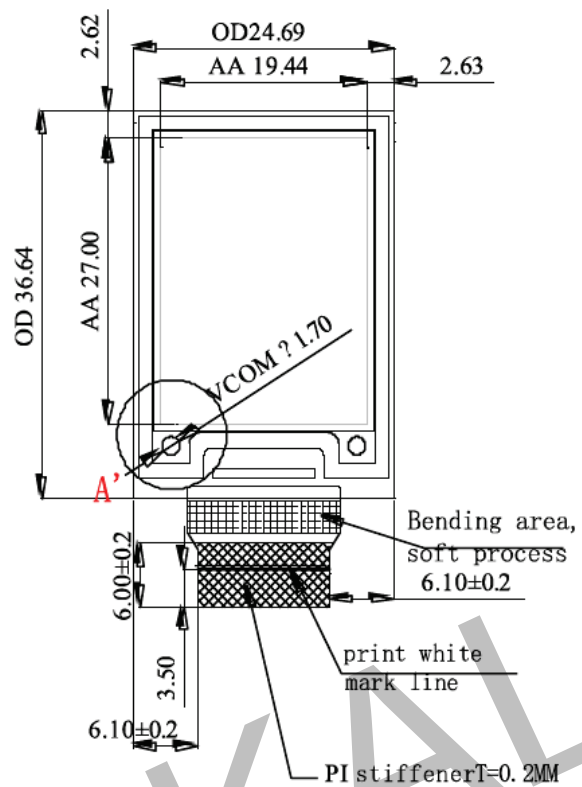




Mechanical Specifications

Parameter	Specifications	Unit	Remark
Screen Size	1.3	Inch	
Display Resolution	144(H)×200(V)	Pixel	Dpi:188
Active Area	19.44(H)×27.00(V)	mm	
Pixel Pitch	0.135×0.135	mm	
Pixel Configuration	Rectangle		
Outline Dimension	24.69(H)×36.64(V) ×1.05(D)	mm	
Weight	1.6±0.2	g	

ABSOLUTE MAXIMUM RATING

able 11-1: Maximum Ratings

Symbol	Parameter	Rating	Unit	Humidity	Unit	Note
V _{CI}	Logic supply voltage	-0.5 to +6.0	V	-	-	
T _{OPR}	Operation temperature range	0 to 40	°C	45 to70	%	Note 11-1
-	Transportation temperature range	-25 to 60	°C	-	-	Note 11-2
Tstg	Storage condition	0 to 40	°C	45 to70	%	Maximum storage time: 5 years
-	After opening the package	0 to 40	°C	45 to70	%	

Power Consumption

Parameter	Symbol	Conditions	TYP	Max	Unit	Remark
Panel power consumption during update	-	25℃	-	40	mAs	-
		0℃	-	100	mAs	
		40℃	-	40	mAs	
Deep sleep mode	-	25℃	-	3	uA	-

mAs=update average current×update time

Input/Output Terminals

Pin #	Single	Description	Remark
1	NC	No connection and do not connect with other NC pins	Keep Open
2	GDR	N-Channel MOSFET Gate Drive Control	
3	RESE	Current Sense Input for the Control Loop	
4	NC	No connection and do not connect with other NC pins	Keep Open
5	VSH2	Positive Source driving voltage	
6	TSCL	I2C Interface to digital temperature sensor Clock pin	
7	TSDA	I2C Interface to digital temperature sensor Date pin	
8	BS1	Bus selection pin	Note 6-5
9	BUSY	Busy state output pin	Note 6-4
10	RES#	Reset	Note 6-3
11	D/C#	Data /Command control pin	Note 6-2
12	CS#	Chip Select input pin	Note 6-1
13	SCL	serial clock pin (SPI)	
14	SDA	serial data pin (SPI)	
15	VDDIO	Power for interface logic pins	
16	VCI	Power Supply pin for the chip	
17	VSS	Ground	
18	VDD	Core logic power pin	
19	VPP	Power Supply for OTP Programming	
20	VSH1	Positive source driver Voltage	
21	VGH	Positive Gate driving voltage	
22	VSL	Negative Source driving voltage	
23	VGL	Negative Gate voltage.	
24	VCOM	VCOM driving voltage	