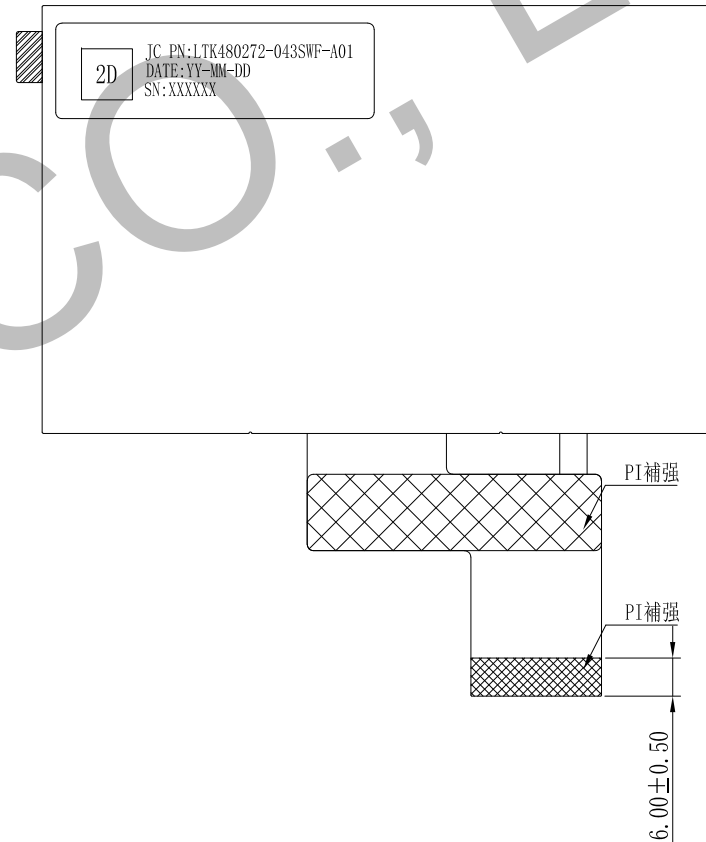
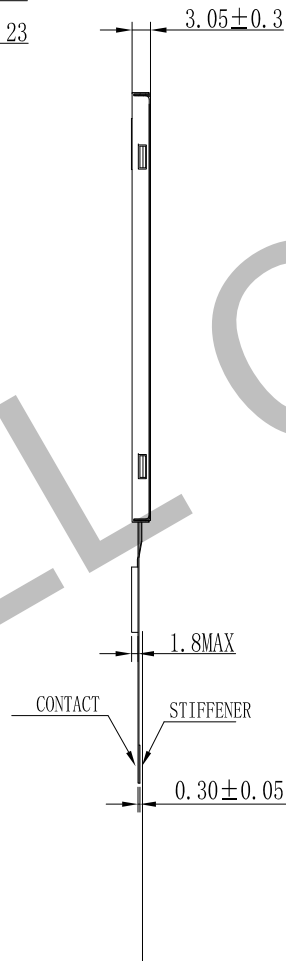
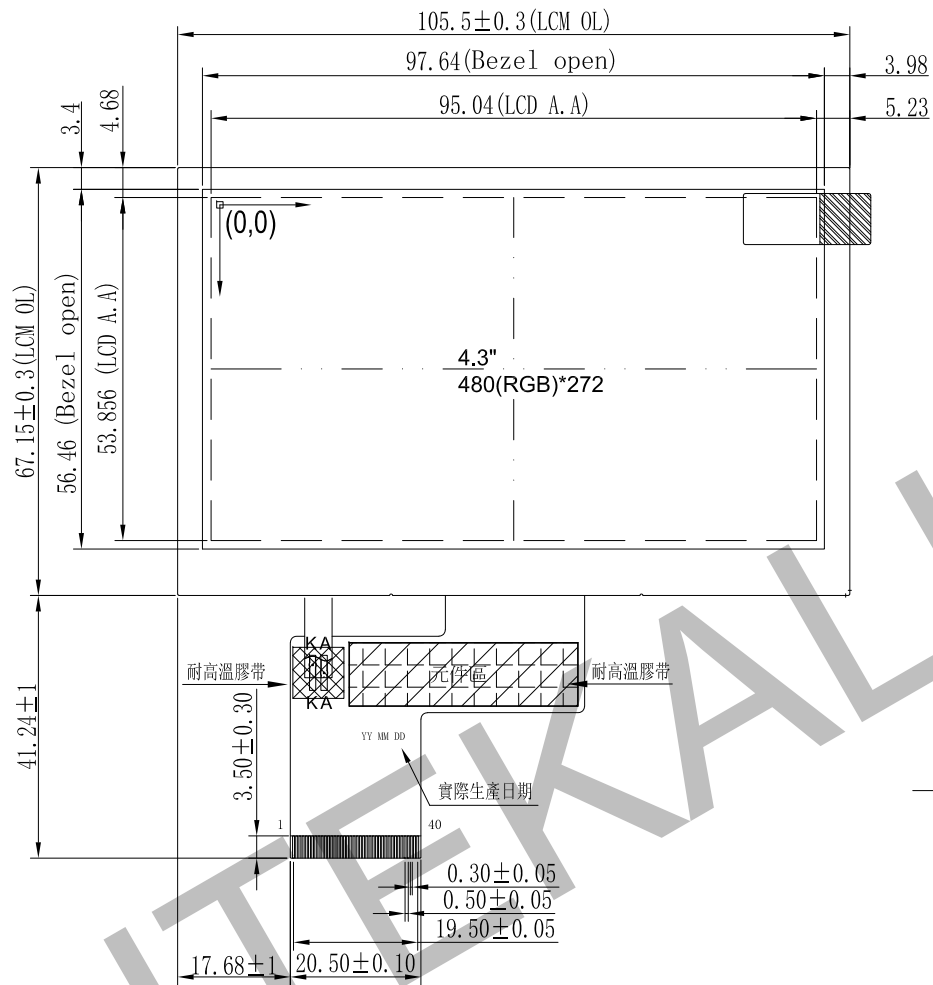




NO.	SYMBOL
Right	Pin Assignment
1	VLED-
2	VLED+
3	GND
4	VDD
5	R0
6	R1
7	R2
8	R3
9	R4
10	R5
11	R6
12	R7
13	G0
14	G1
15	G2
16	G3
17	G4
18	G5
19	G6
20	G7
21	B0
22	B1
23	B2
24	B3
25	B4
26	B5
27	B6
28	B7
29	GND
30	PCLK
31	DISP
32	HSYNC
33	VSYNC
34	DE
35	NC
36	GND
37	VPP
38	CSX
39	SCL
40	SDA

General specification

Item	specifications	Unit
Active screen size	4.3 inch Diagonal	-
Display resolution	480x RGB x 272	pixel
Module size (W x H x T)	105.5×67.15 ×3.05	mm
Active area (W x H)	95.04x53.856	mm
Pixel pitch (W×H)	0.198x 0.198	mm
Interface	RGB -24BIT	-
Module weight	TBD±10%	g
Our components and processes are compliant to RoHS standard.		

Item	specifications
LCD type	a-Si TFT
LCD mode	Normally Black
Polarizer mode	Transmissive
Pixel arrangement	RGB-stripe
Backlight type	LED
Viewing direction(Gray inversion)	ALL

Interface pin

Pin No.	Symbol	Function
1	VLED-	Cathode of Backlight
2	VLED+	Anode of Backlight
3	GND	Ground
4	VDD	Supply voltage to the analog circuit.(+3.3V)
5~12	R0~R7	8 Bit data bus display red data
13~20	G0~G7	8 bit data bus display green data
21~28	B0~B7	8 bit data bus display blue data
29	GND	Ground
30	PCLK	Pixel clock input pin
31	DISP	DISP sets the display mode ; DISP=0 : Standby mode ; DISP=1 : Normal display mode
32	HSYNC	Horizontal sync signal, default is negative polarity
33	VSYNC	Vertical sync signal, default is negative polarity
34	DE	Data input enable. Display access is enabled when DE is “H”.
35	NC	Not Connect
36	GND	Ground
37	VPP	Power input pin for NVM. When writing NVM, it needs external power supply voltage (10.35V).
38	CSX	Serial communication chip select, Internal pull high
39	SCL	Serial communication clock input, Internal pull low.
40	SDA	Serial communication data input and output ,Internal pull low