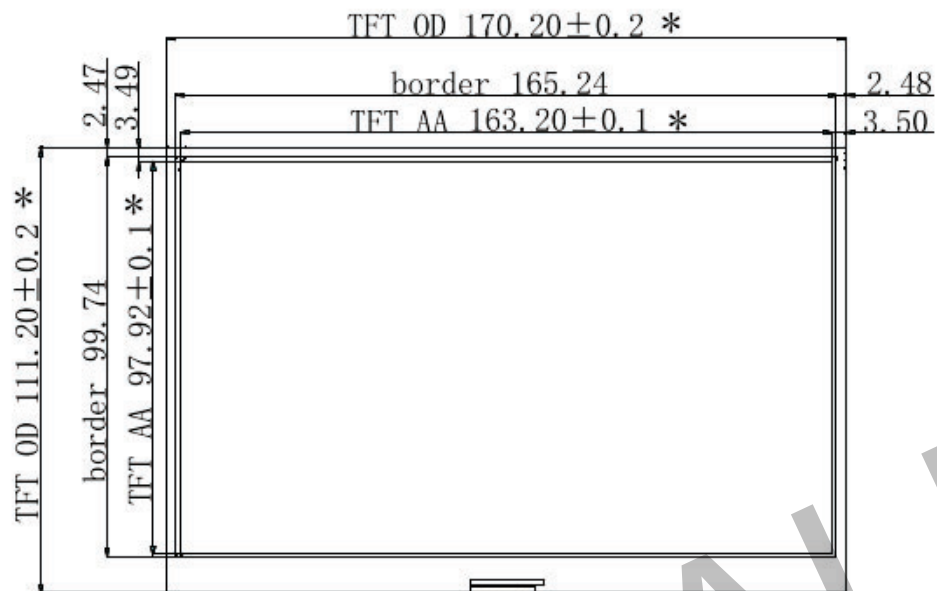
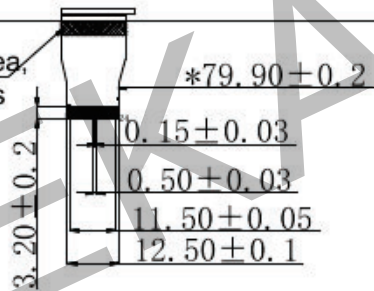




Bending area,
soft process

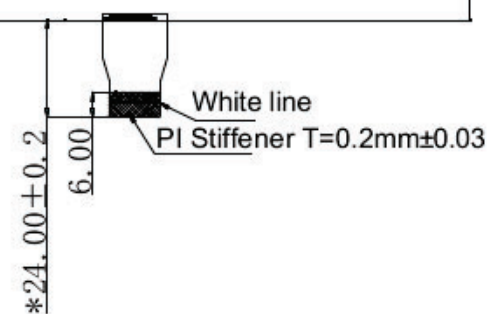


Total 1.25 ± 0.1 *

FPC 0.10 ± 0.03

FPC+PI Stiffener

Total Thickness 0.30 ± 0.03 *



Mechanical Specifications

Parameter	Specifications	Unit	Remark
Screen Size	7.5	Inch	
Display Resolution	800(H)×480(V)	Pixel	Dpi:124
Active Area	163.20(H)×97.92(V)	mm	
Pixel Pitch	0.204×0.204	mm	
Pixel Configuration	Square		
Outline Dimension	170.20(H)×111.20 (V) ×1.1 (D)	mm	Without masking film
Weight	44±0.5	g	

Absolute maximum rating

Symbol	Parameter	Rating	Unit	Humidity	Unit	Note
V _{DD}	Logic supply voltage	-0.5 to +6.0	V	-	-	
T _{OPR}	Operation temperature range	0 to 40	°C	45 to70	%	Note 8-1
-	Transportation temperature range	-25 to 60	°C	-	-	Note 8-2
T _{stg}	Storage condition	0 to 40	°C	45 to70	%	Maximum storage time: 5 years
-	After opening the package	0 to 40	°C	45 to70	%	

Power Consumption

Parameter	Symbol	Conditions	TYP	Max	Unit	Remark
Panel power consumption during update	-	25℃		250	mAs	-
		0℃		450	mAs	
		40℃		250	mAs	
Deep sleep mode	-	25℃		3	uA	-

mAs=update Average current × update time

Input/Output Terminals

Pin #	Single	Description	Remark
1	NC	No connection and do not connect with other NC pinss	Keep Open
2	GDR	This pin is N-MOS gate control.	
3	RESE	Current Sense Input for the Control Loop	
4	NC	No connection and do not connect with other NC pins	
5	VSHR	Positive source voltage for Red	
6	TSCL	I2C clock for external temperature sensor	Note 6-6
7	TSDA	I2C clock for external temperature sensor	Note 6-6
8	BS	Input interface setting. Select 3 wire/ 4 wire SPI interface	Note 6-5
9	BUSY_N	This pin indicates the driver status	Note 6-4
10	RST_N	Global reset pin. Low reset	Note 6-3
11	DC	Serial communication Command/Data input	Note 6-2
12	(CSB)CS-N	Serial communication chip select	Note 6-1
13	(SCL)CLK	serial clock pin (SPI)	
14	(SDA)DATA	serial data pin (SPI)	
15	VDDIO	Power for interface logic pins	
16	VDDA(VDD)	Power Supply pin for the chip	
17	VSS	Digital ground	
18	VDDD	Core logic power pin	
19	(FMSDO)VPP	OTP program power	
20	VSH	Positive Source driving voltage	
21	VGH	Positive Gate driving voltage	
22	VSL	Negative Source driving voltage	
23	VGL	Negative Gate voltage.	
24	VCOM	VCOM driving voltage	