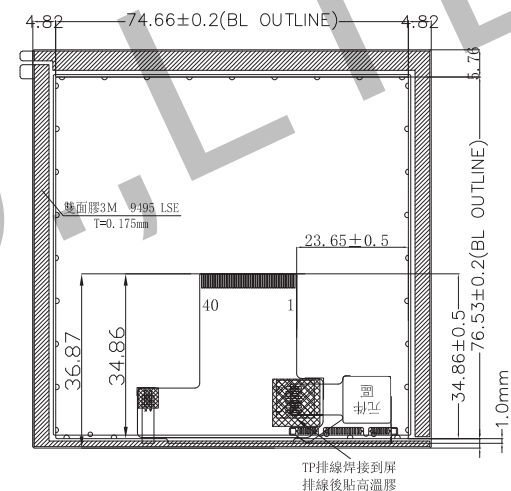
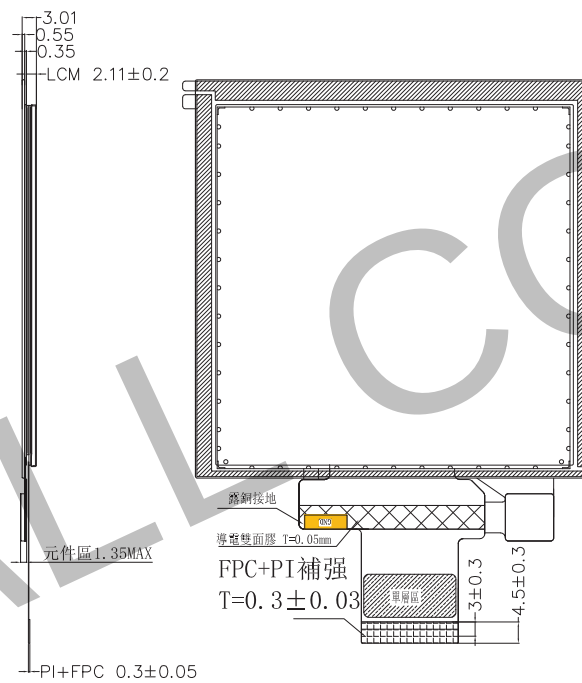
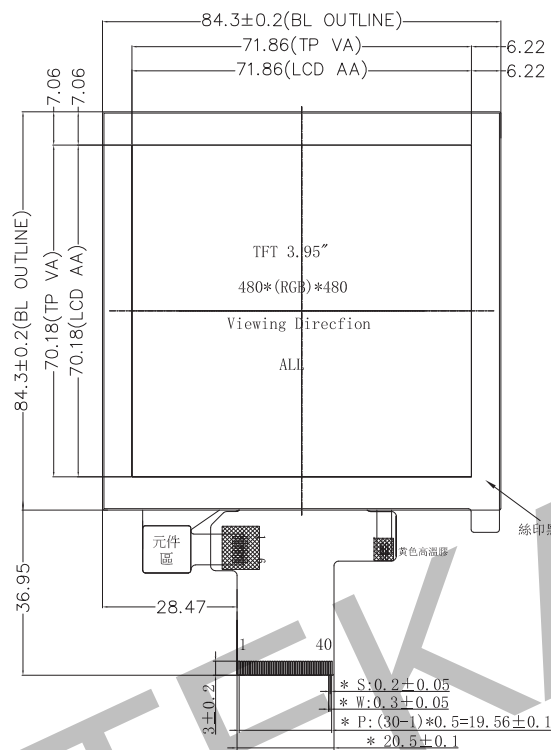




General specification

Item	specifications	Unit
Active screen size	3.95 inch Diagonal	-
Display resolution	480 x RGB x 480	pixel
Module size (W x H x T)	84.30×84.30 × 3.01(Exclude FPC)	mm
Active area (W x H)	71.86 × 70.18	mm
Pixel pitch (W×H)	0.149 x 0.146	mm
Interface	3SPI_18BIT_RGB	-
Module weight	TBD±10%	g
Our components and processes are compliant to RoHS standard.		

Item	specifications
LCD type	a-Si TFT
LCD mode	IPS / Normal Black
Polarizer mode	Transmissive
Pixel arrangement	RGB-stripe
Backlight type	LED
Viewing direction(Gray inversion)	ALL

Interface pin

Description

Pin No.	Symbol	Function
1	LED A	Power supply for backlight cathode input terminal.
2	LED K1	Power supply for backlight anode input terminal.
3	LED K2	Power supply for backlight anode input terminal.
4	GND	Ground for logic.
5	VCI	VCC power input(2.8V-3.3V)
6	RESET	Reset signal input terminal, active at 'L'.
7-8	NC	NC
9	SDA	Serial input signal in SPI I/F.
10	SCL	This pin is used serial interface clock in SPI
11	CS	Chip select input pin ("Low" enable).
12	PCLK	Dot clock signal input. Latching input data at its rising edge.
13	DE	Data enable signal for RGB interface operation.
14	VS	Vertical sync input. Negative polarity.
15	HS	Horizontal sync input. Negative polarity.
16-33	DB0-DB17	DATA BUS
34	GND	Ground for logic.
35	TP_INT	CTP-INT
36	TP_SDA	CTP-SDA
37	TP_SCL	CTP-SCL
38	TP_RST	CTP-RESET
39	TP_VCC	Power supply for analog of CTP (2.8V-3.3V)
40	GND	Ground for logic.