



General specification

Item	specifications	Unit
Active screen size	10.17 inch Diagonal	-
Display resolution	1920 x RGB x 532	pixel
Module size (W x H x T)	260.28 x 86.23 x7.95	mm
Active area (W x H)	248.83 x 68.95	mm
Pixel pitch (W×H)	0.1296 x 0.1296	mm
Interface	LVDS	-
Module weight	TBD±10%	g
Our components and processes are compliant to RoHS standard.		

Item	specifications
LCD type	a-Si TFT
LCD mode	IPS / Normal black
Polarizer mode	Transmissive
Pixel arrangement	RGB-stripe
Backlight type	LED
Viewing direction(Gray inversion)	85/85/85/85

Interface pin

Pin No.	Symbol	Function
1~4	GND	Ground
5	NC	/
6	VGL	Power supply for analog circuit.
7	NC	/
8	VGH	Power supply for analog circuit.
9	NC	/
10	VCOM	Internal driving circuit power for LCD common electrode
11	NC	/
12	VPP	Keep Floating, used for LCM maker
13	NC	/
14	VSP(VDPA)	Power supply for SD circuit.
15	VSP(VDPA)	Power supply for SD circuit.
16	NC	/
17	VSN(VDNA)	Power supply for SD circuit.
18	VSN(VDNA)	Power supply for SD circuit.
19	NC	/
20	VDDN1	Power setting capacitor connected pins.
21	I2C_SCL	Serial communication clock pin for I2C interface
22	I2C_SDA	Serial Interface address and data input/output for I2C interface.
23	E2PEN	Register initialization source.
24	ROM_CS	EEPROM communication data pin
25	ROM_CLS	EEPROM communication clock pin
26	ROM_SDO	EEPROM communication data pin
27	ROM_SDI	EEPROM communication data pin
28	VDD	Power input for main and I/O power
29	DVDD	Internal regulator output for logic power supply
30	DVDD_IF	Internal regulator output for interface power supply
31	GND	Ground.
32	OLV3P	LVDS data lane 3 Positive
33	OLV3N	LVDS data lane 3 Negative
34	GND	Ground.
35	OLVCLKP	LVDS Clock Lane Positive
36	OLVCLKN	LVDS Clock Lane Negative
37	GND	Ground.
38	OLV2P	LVDS data lane 2 Positive
39	OLV2N	LVDS data lane 2 Negative
40	GND	Ground.
41	OLV1P	LVDS data lane 1 Positive
42	OLV1N	LVDS data lane 1 Negative
43	GND	Ground.
44	OLV0P	LVDS data lane 0 Positive
45	OLV0N	LVDS data lane 0 Negative
46	GND	Ground.
47	VGMPhi	Positive gamma reference voltage
48	VGMPLI	Positive gamma reference voltage
49	VGMNHI	Positive gamma reference voltage
50	VGMNLI	Positive gamma reference voltage
51	ERROPB	For ASIL detection results output. H : abnormal. L : normal.
52	STBYB	Standby mode. H : normal operation. L : Stbybmode

53	GRB	Global reset pin. H : normal operation. L : Reset
54	UPDN	For Up or Down sequence control. H : U2D sequence L : D2U sequence
55	SHLR	Source Right or Left sequence control. H : Left→Right L : Right→Left
56~57	NC	/
58	BIST	Keep pull low, used for LCM maker
59	VCOM	Internal driving circuit power for LCD common electr ode
60	GND	Ground.